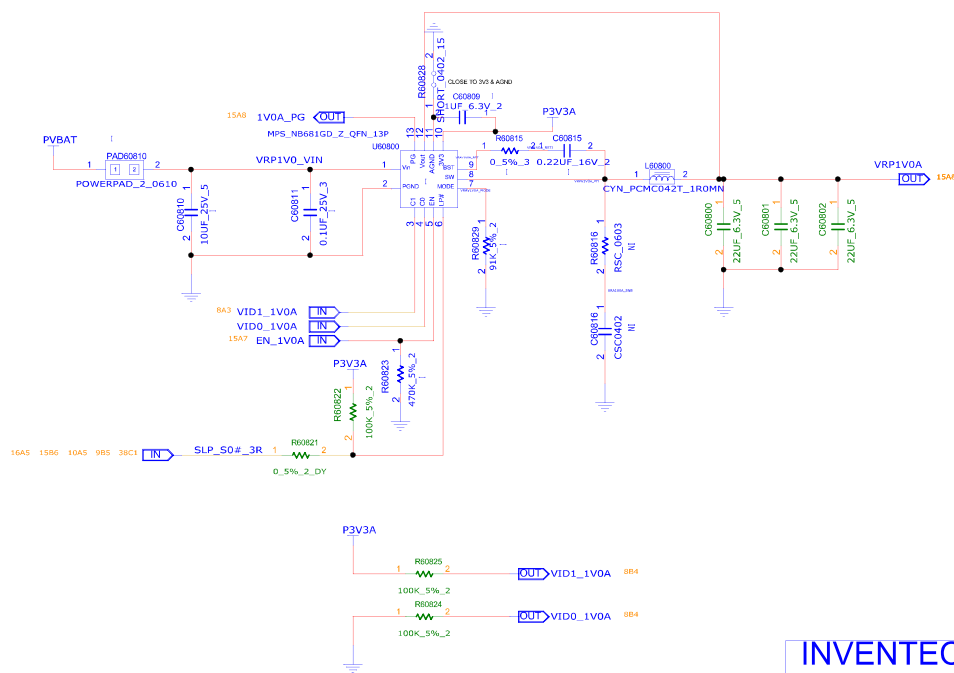






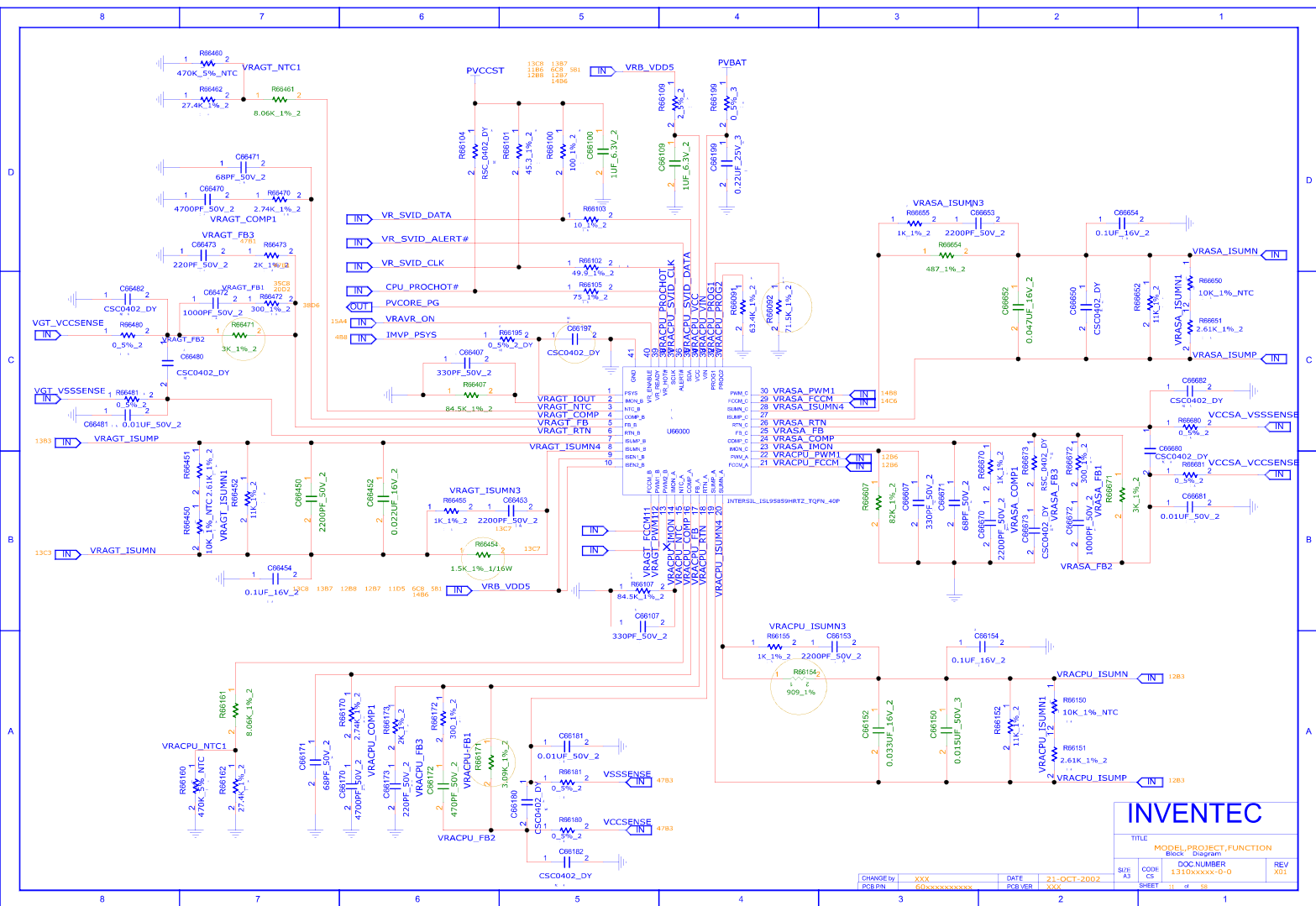


	LP#	C1	C0	VOUT(V)
VCCIO	0	X	X	0
	1	0	0	0.85
	1	0	1	0.875
	1	1	0	0.95
	1	1	1	0.975
VCCPRIM CORE	0	X	X	0.7
	1	0	0	0.85
	1	0	1	0.9
	1	1	0	0.95
	1	1	1	1.00
EDRAM/ EOPIO	0	X	X	0
	1	0	0	0.8(MSM)
	1	0	1	0.95
	1	1	0	1
	1	1	1	1.05
Others	0	X	X	0
	1	0	0	1.0
	1	0	1	1.075
	1	1	0	1.15
	1	1	1	1.2





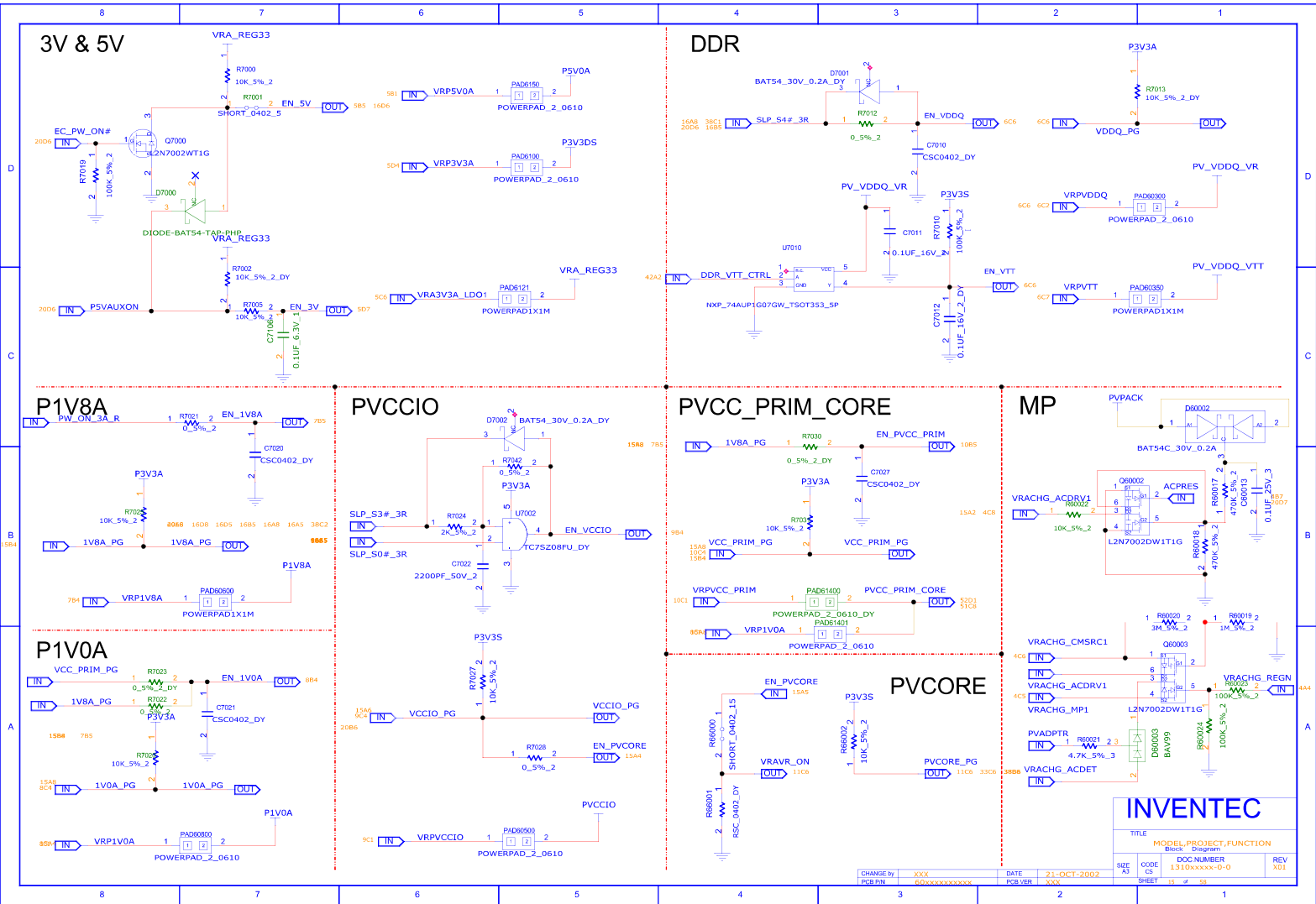


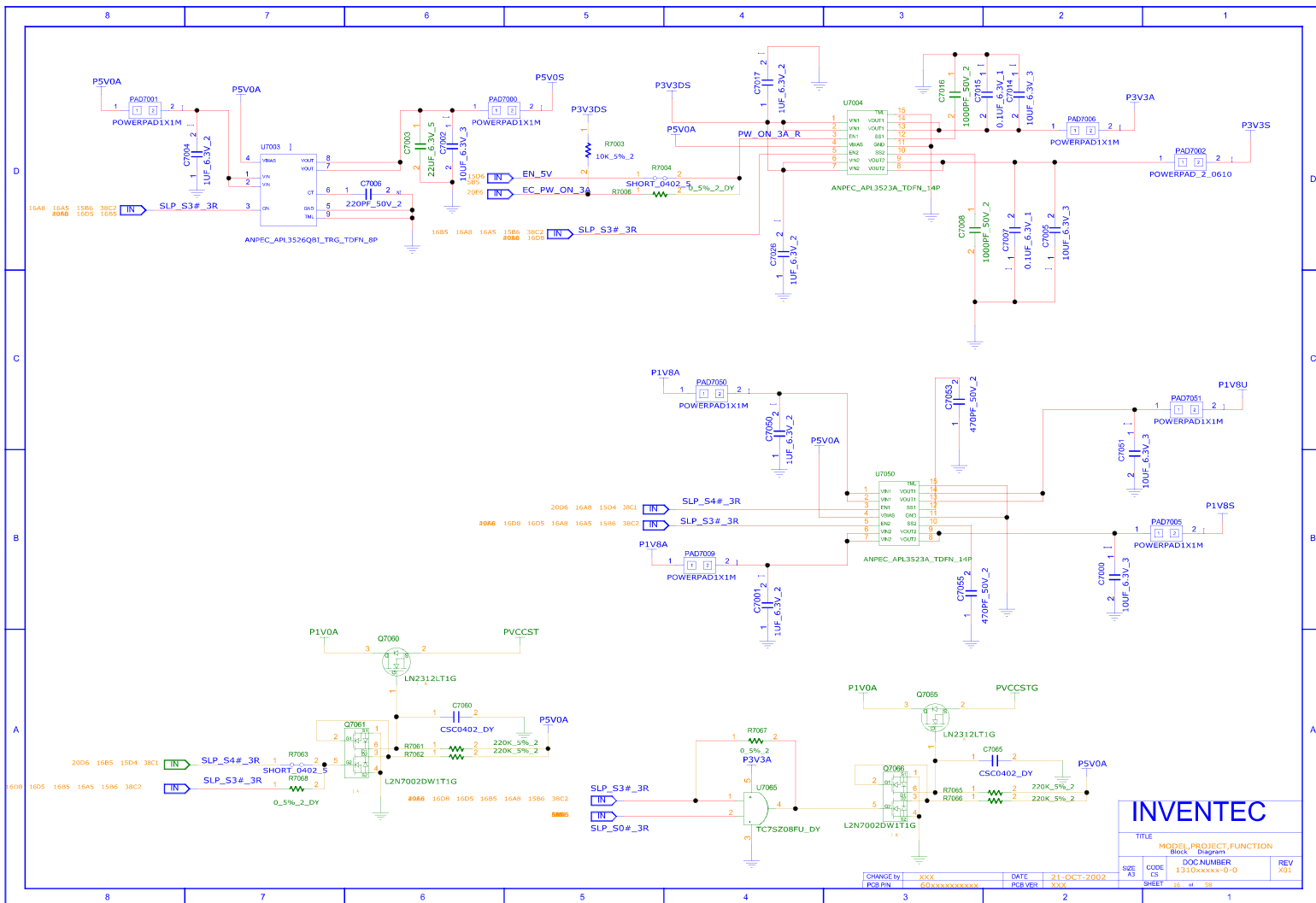


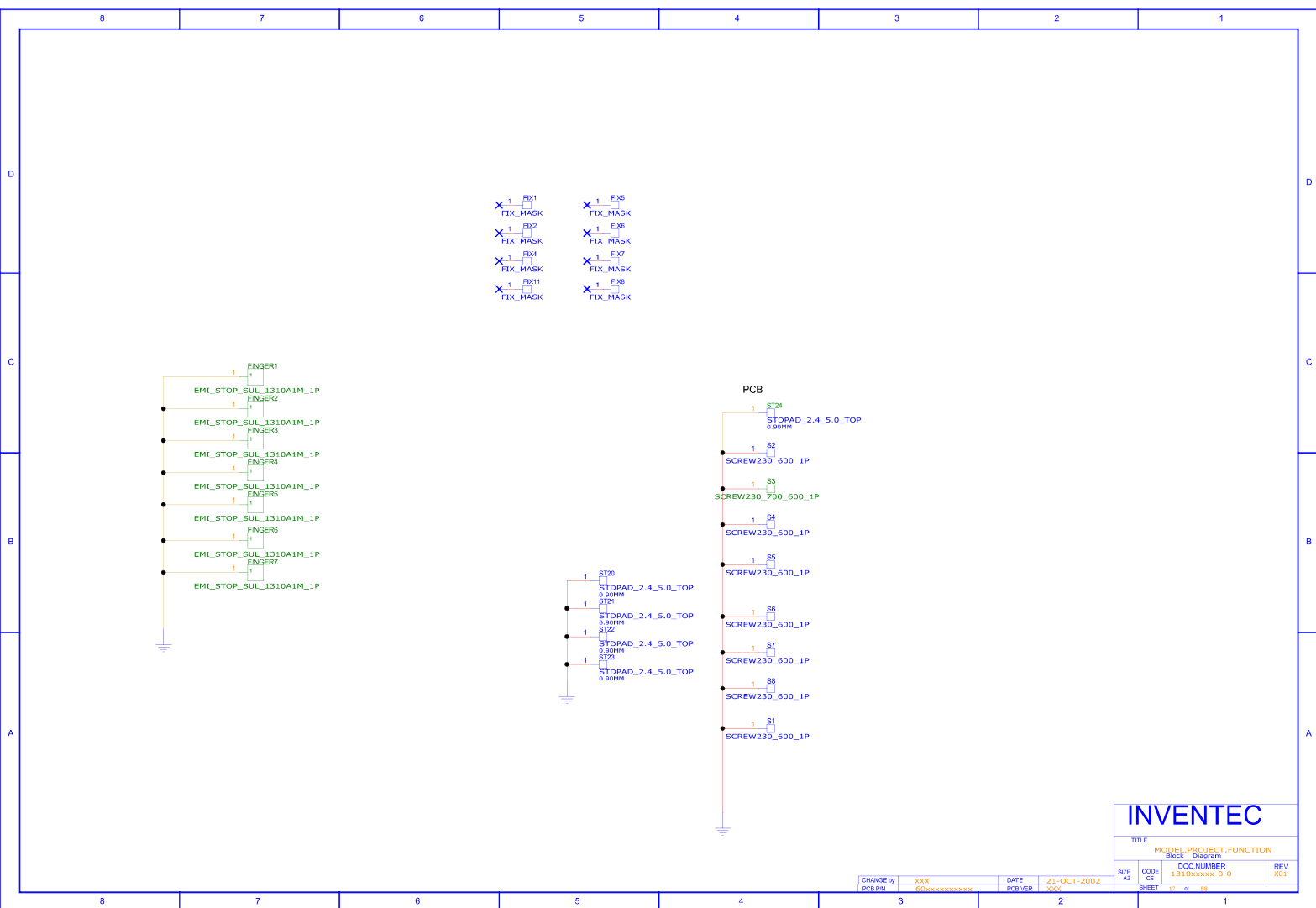


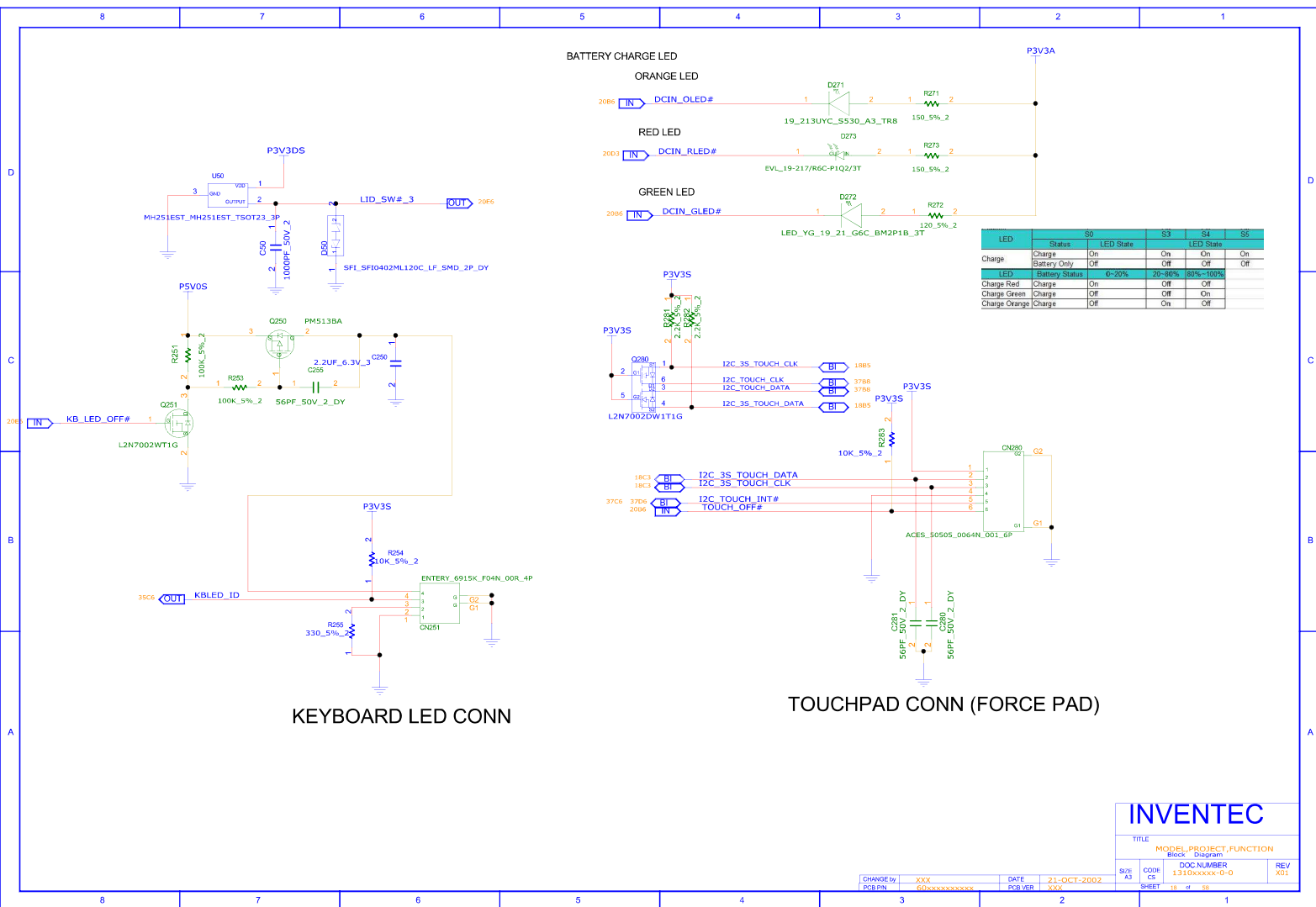




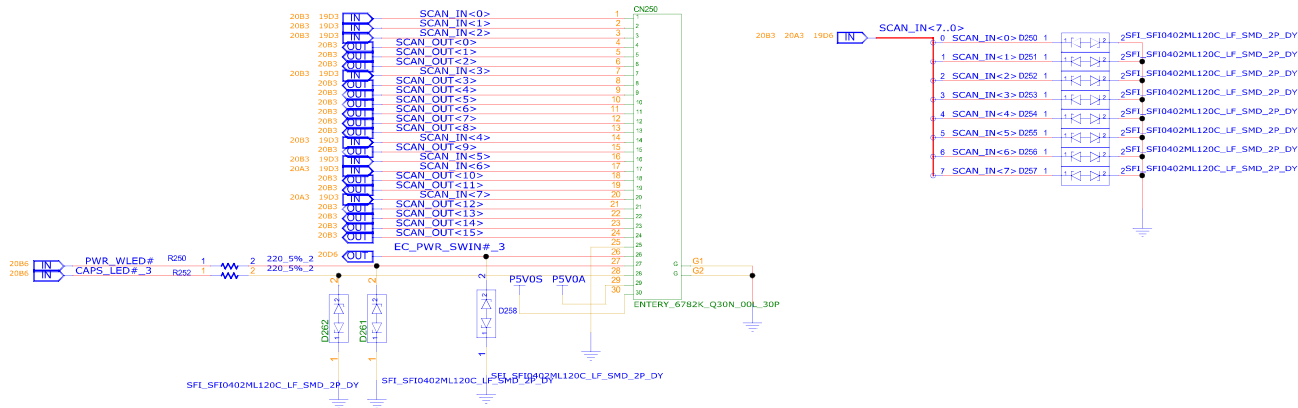




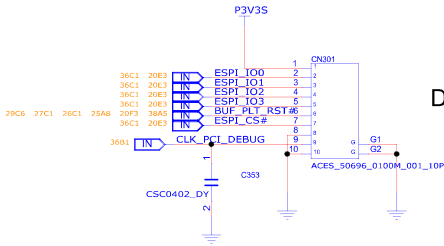




REFERENCE 200~249(POWER CONN)  
REFERENCE 250~299(KB/TP CONN)



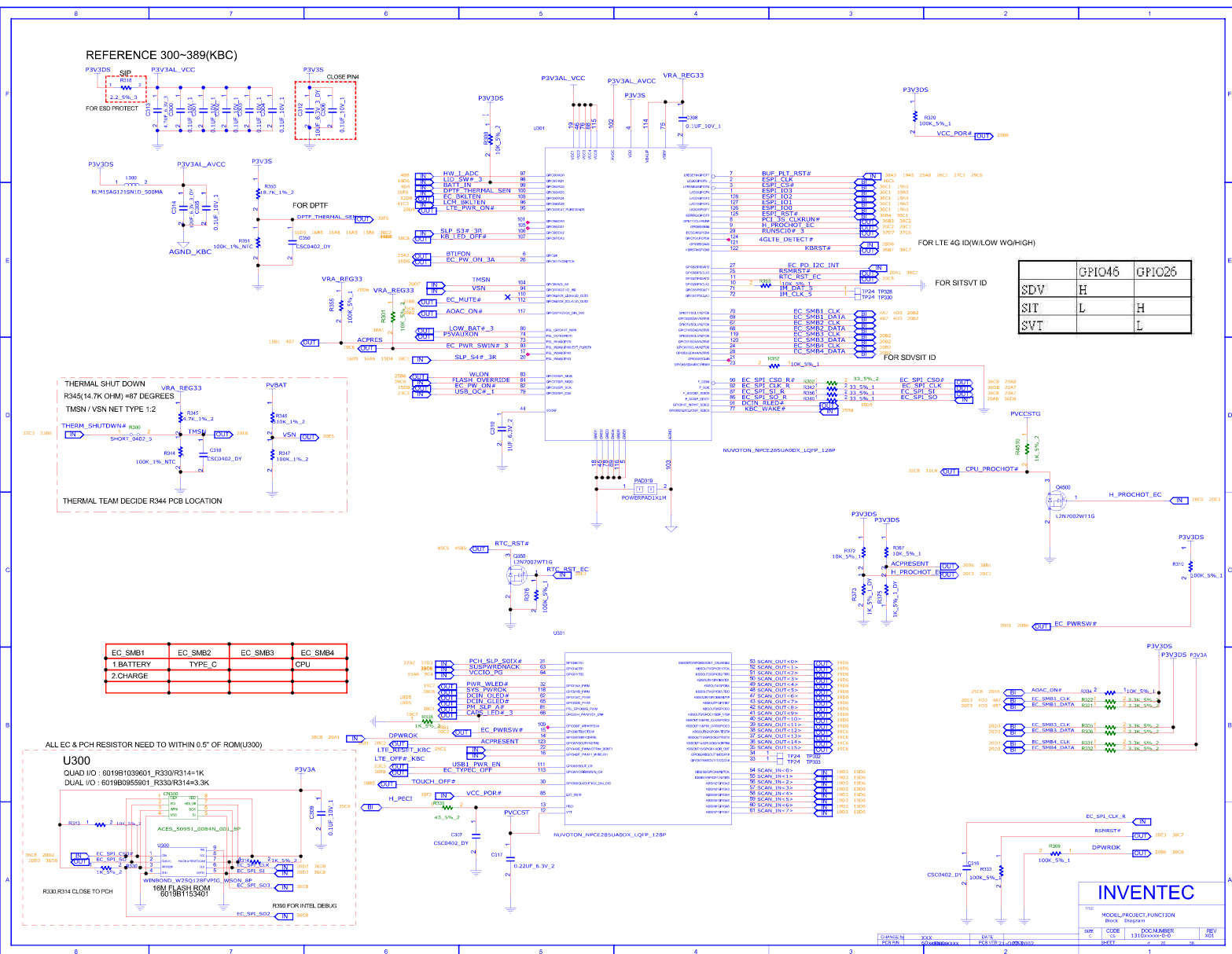
KEYBOARD CONN

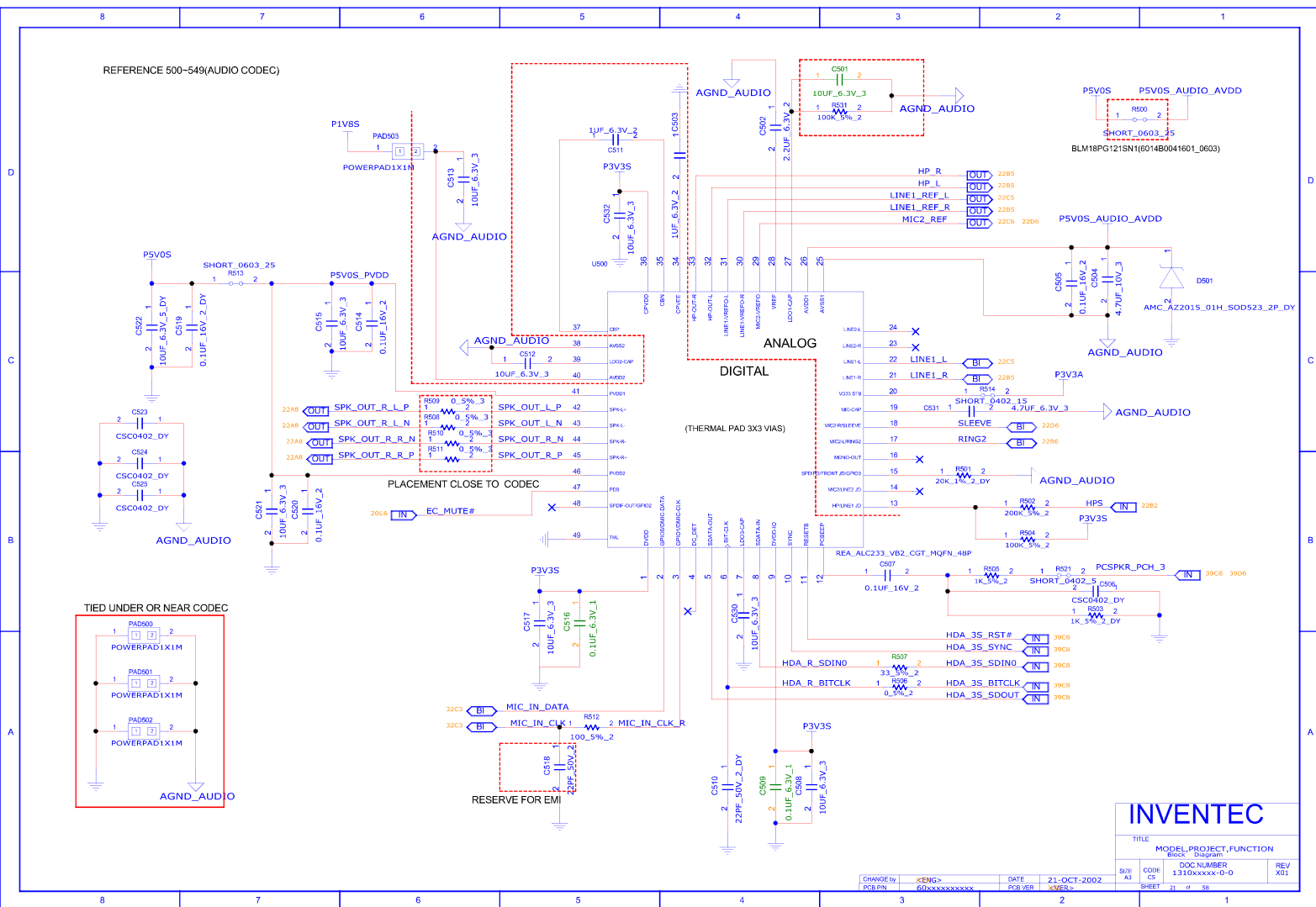


DEBUG CONN

INVENTEC				
TITLE				
MODEL PROJECT_FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310XXXX-0-0	X01	
SHEET		of 10	58	

CHANGE BY: XXX, DATE: XXX-XXX-XXX, PCB VER: XXX-XXX-XXX, PCB DN: XXX-XXX-XXX

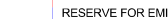




REFERCE 600~649(JACK/MIC/SPEAKER)



NOTE:SPK TRACE SHOULD 30~40 MILS WIDTH



CHANGE by	xxx
PCB P/N	60xxxxxxxxxxx

DATE
PCB Y

VER	2
-----	---

21-OCT  
XX

1-2002

Size	
Area	

CS  
SHEE

E	13
T	22

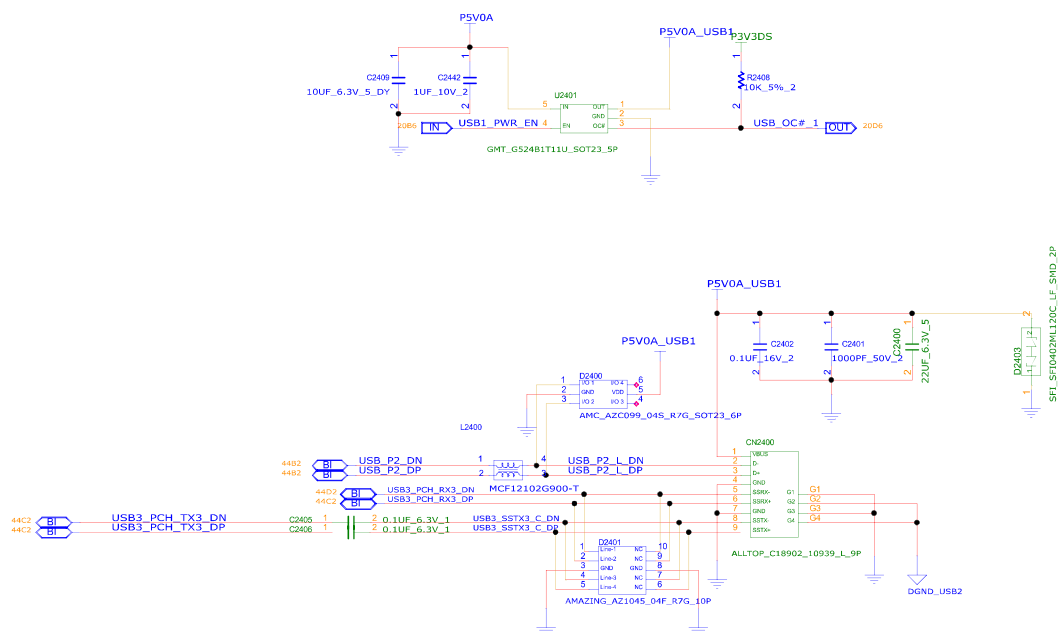
10xxxx

58

0-0-0

X01	
-----	--

REFERENCE 2400~2450(USB3.0)

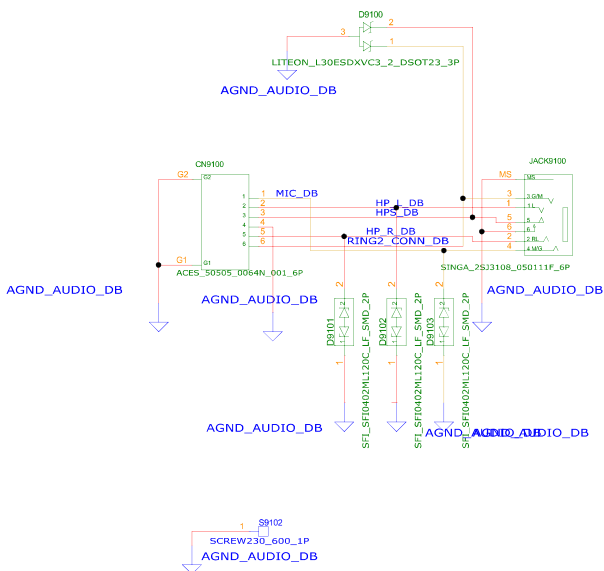


INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 33 of 50			

CHANGE by	XXX	DATE	21-OCT-2002
PCB PIN	60XXXXXXXXXXXX	PCB VER	XXX

AUDIO JACK SMALL BOARD



- 1 FIX9100
- 1 FIX\_MASK\_0.8
- 1 FIX9101
- 1 FIX\_MASK\_0.8
- 1 FIX9102
- 1 FIX\_MASK\_0.8
- 1 FIX9103
- 1 FIX\_MASK\_0.8

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET		24 of 58	

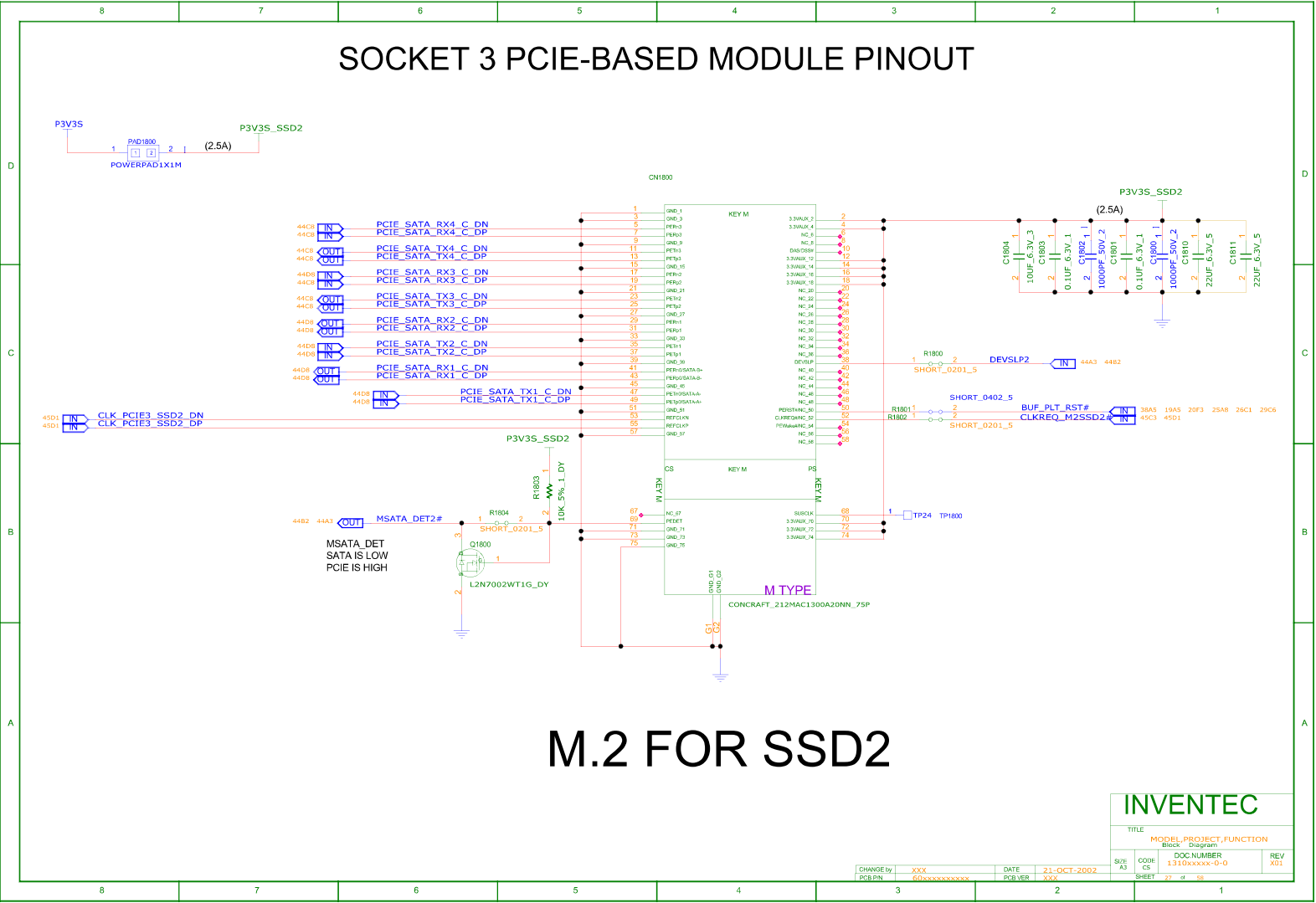
CHANGE BY	XXX	DATE	21-OCT-2002
POB DN	XXXXXXXXXXXX	POB VER	XXX

## REFERENCE 1300 ~ 1349





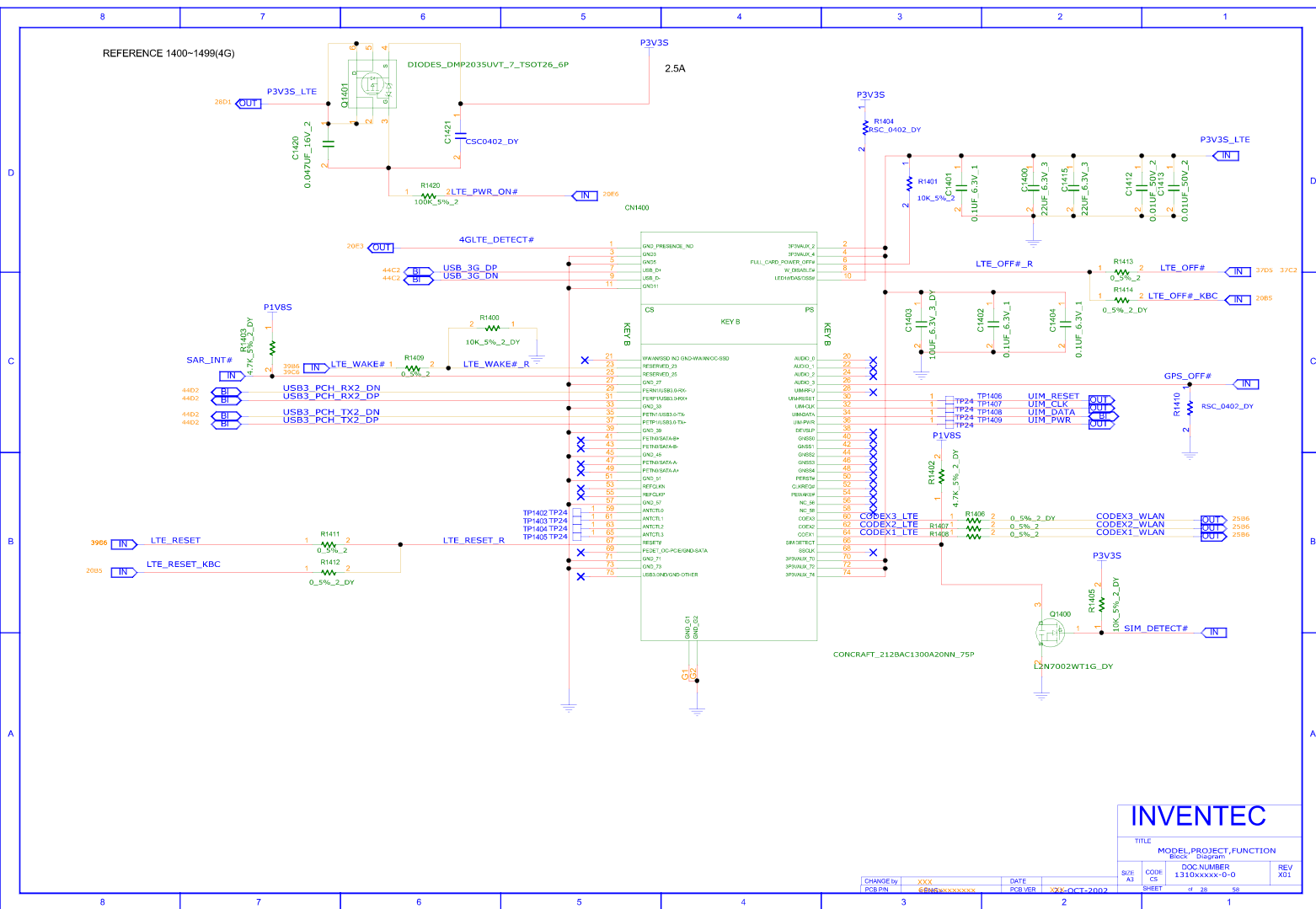
SOCKET 3 PCIE-BASED MODULE PINOUT

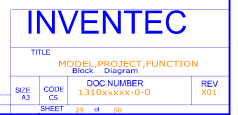


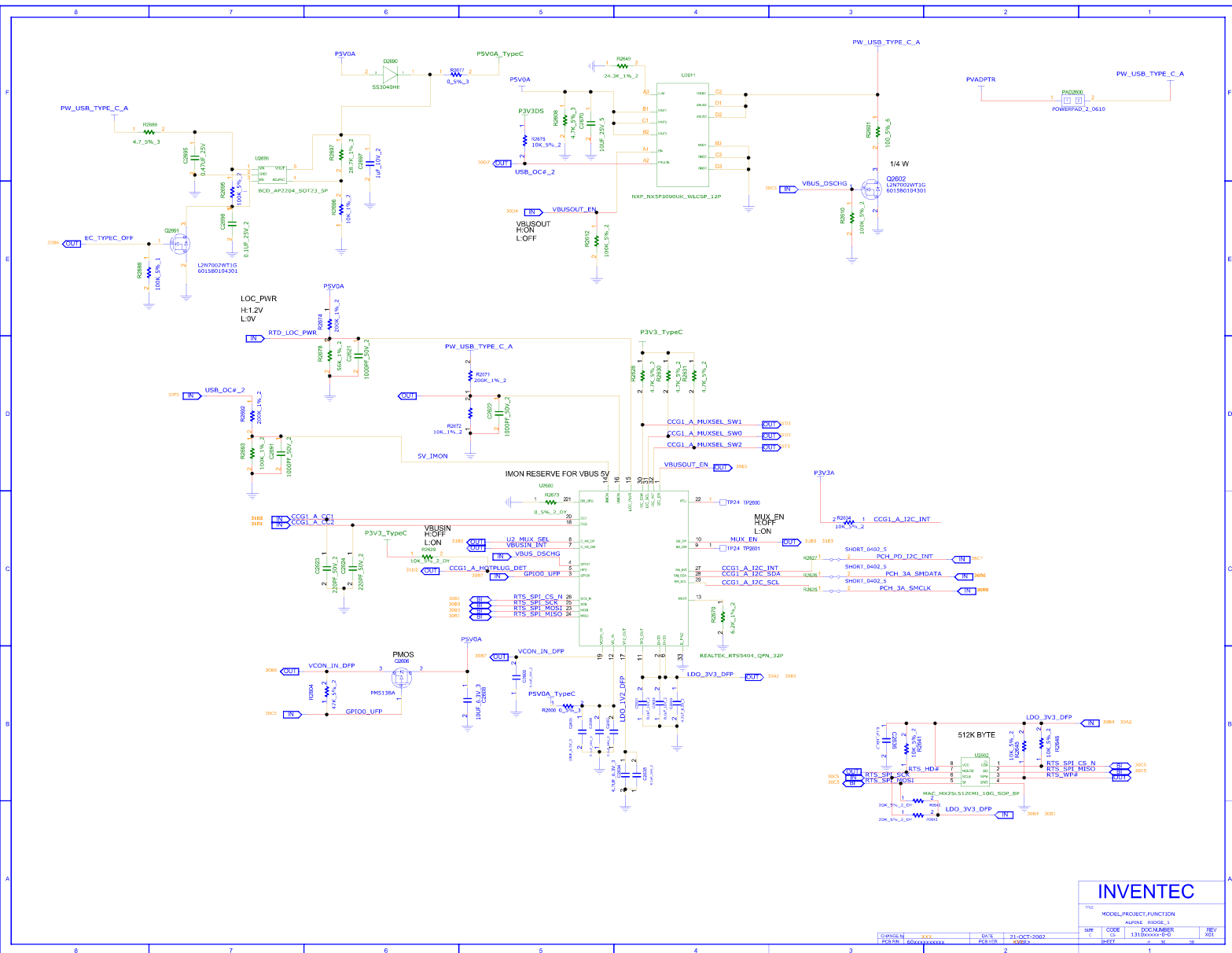
M.2 FOR SSD2

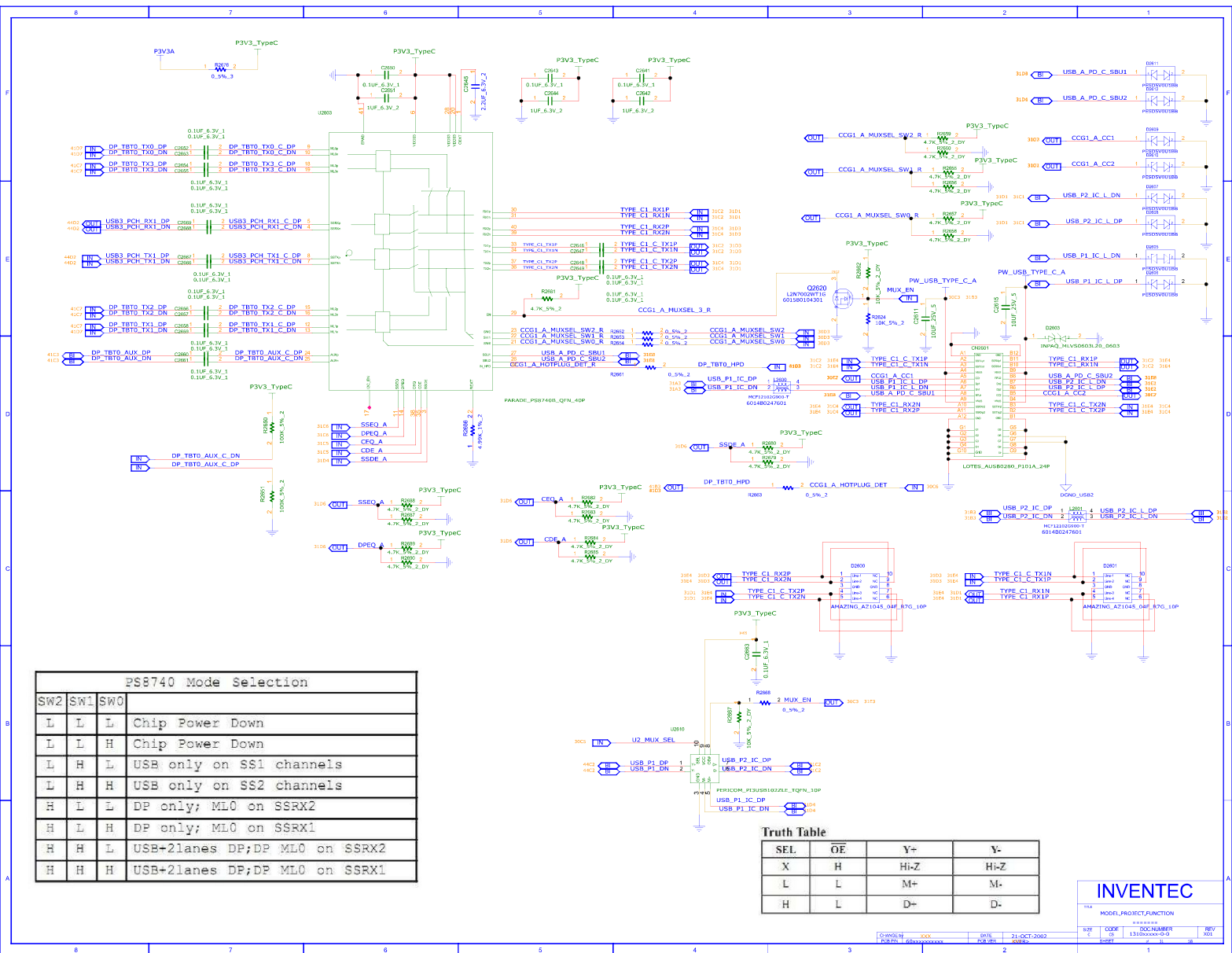
INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxxx-0-0	X01	
SHEET 27 of 58				

CHANGE BY	XXX	DATE	21-OCT-2002
PCB DN	60xxxxxxxxxx	PCB VER	XXX



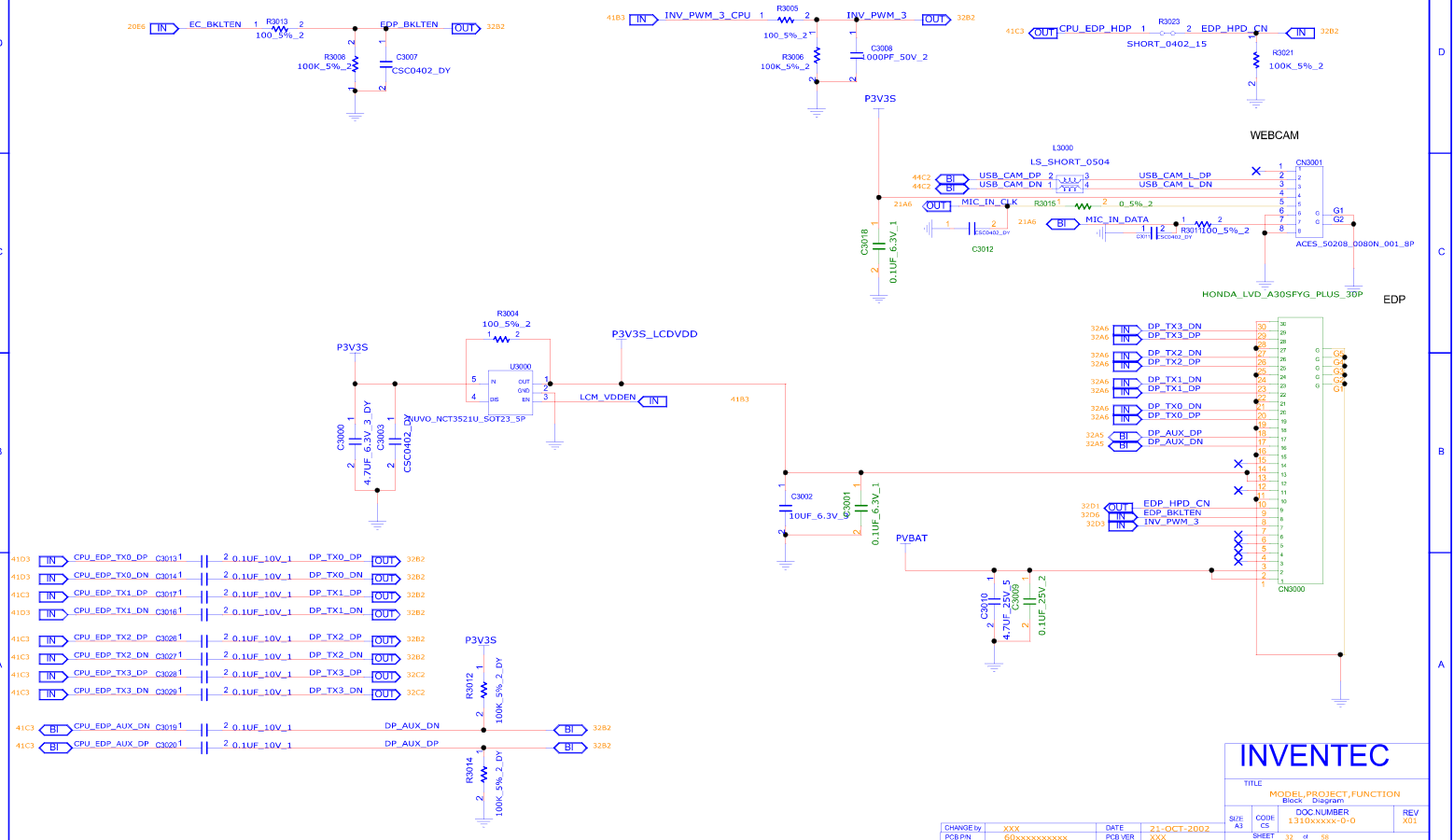




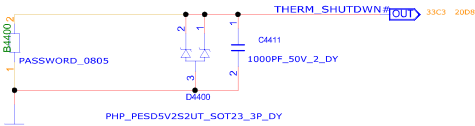
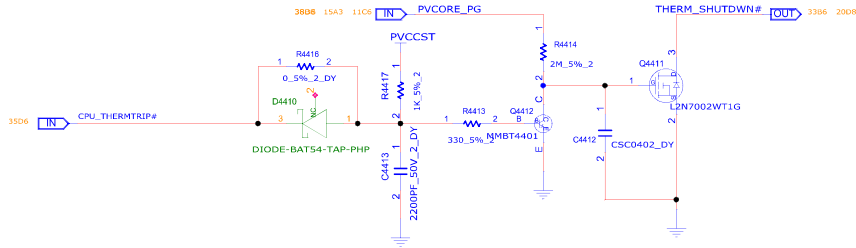


REFERENCE 3000~3049(LCM)

# EDP CONN



REFERENCE 4300~4349(FAN)  
REFERENCE 4411~4449(THERMAL )

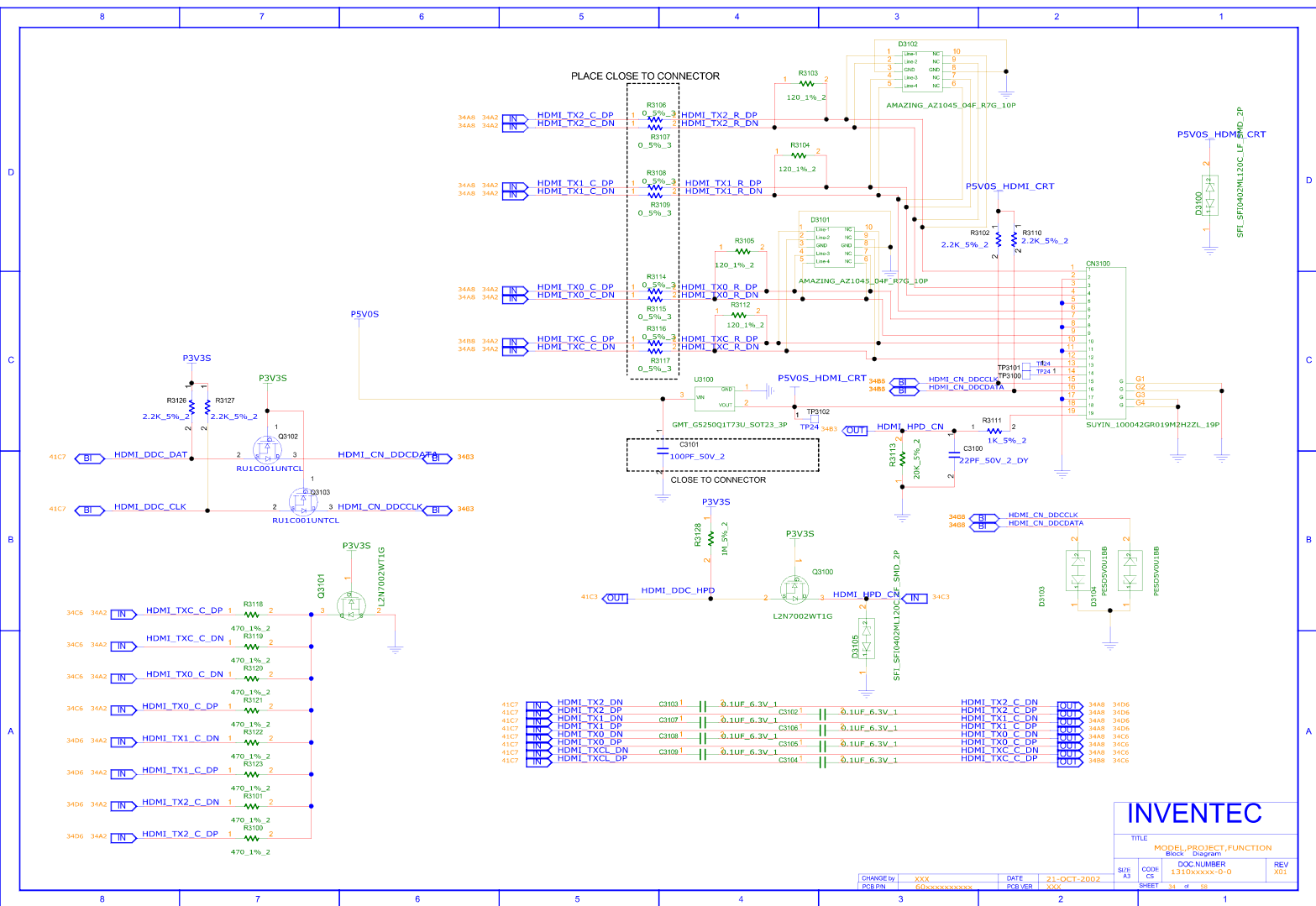


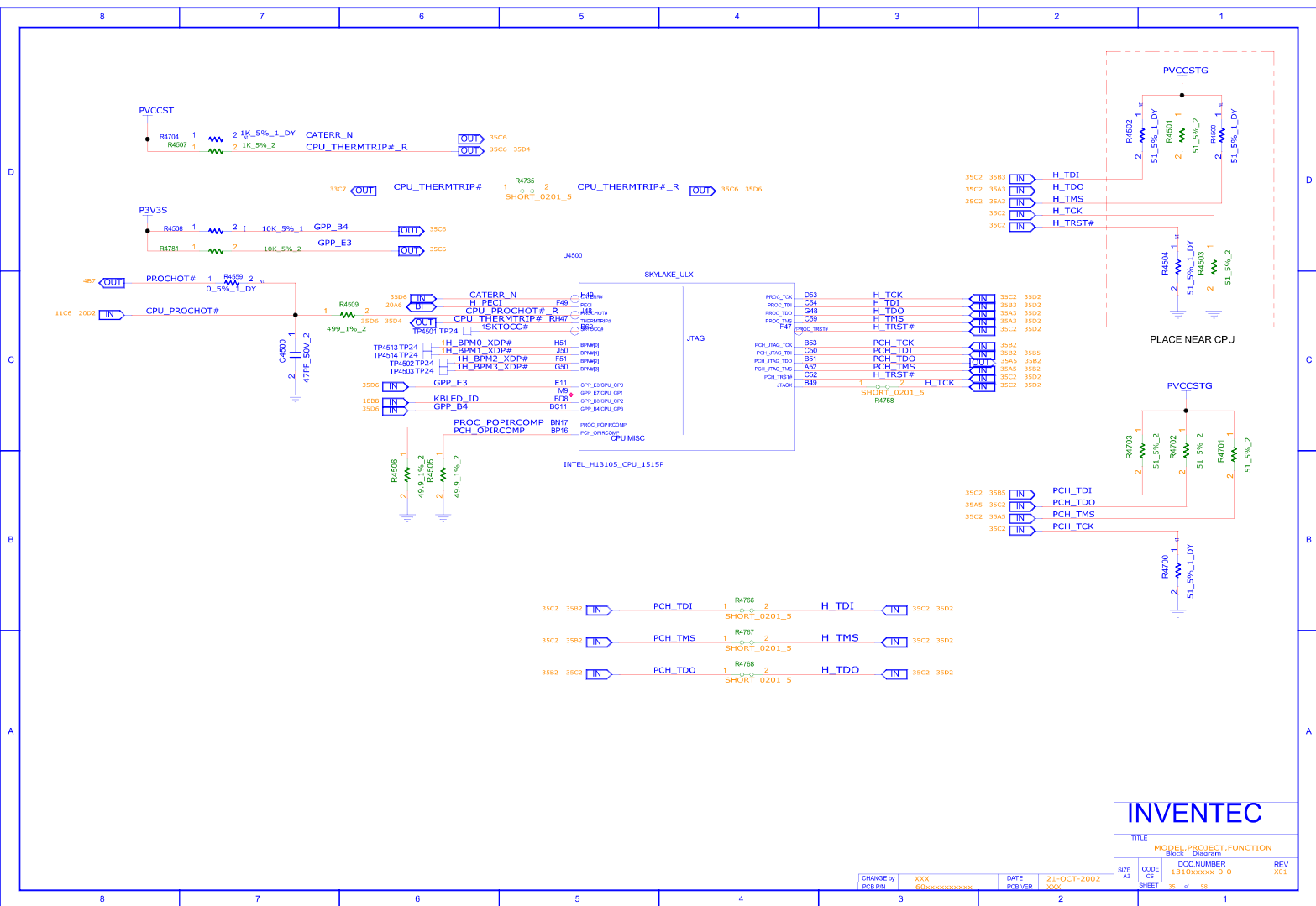
POWER OFF SWITCH

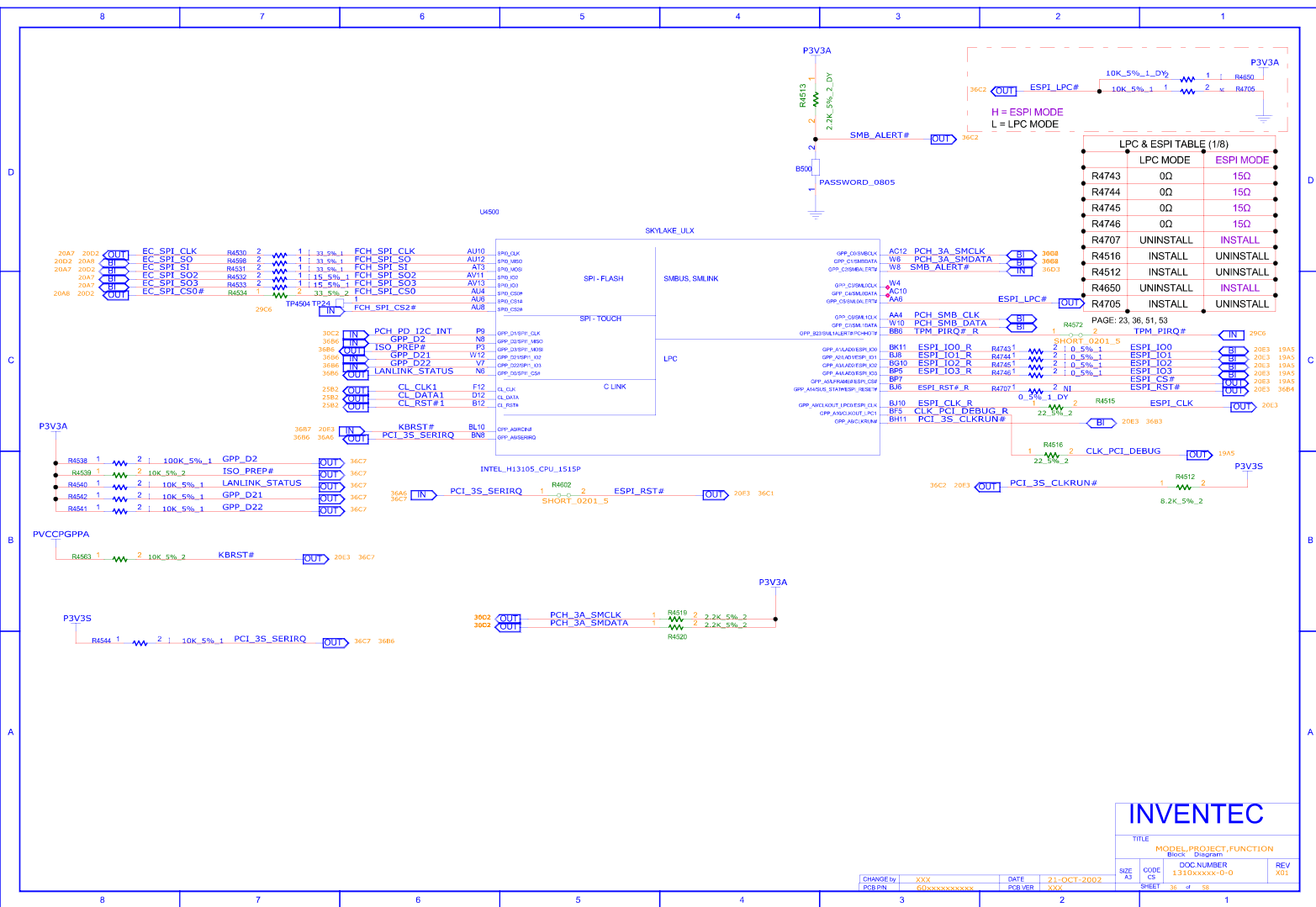
REFERENCE NUMBER:4411~4449

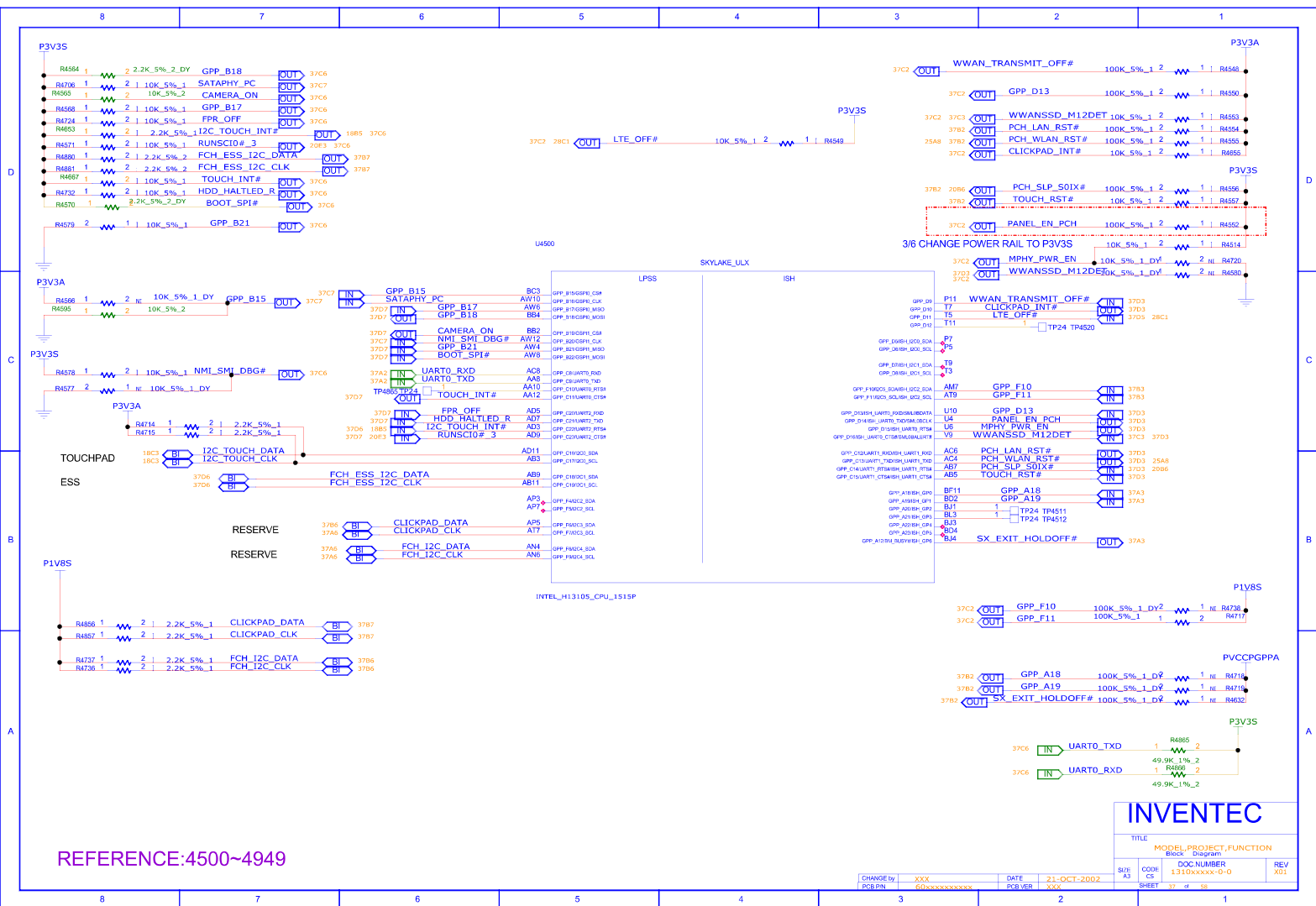
INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		of 33 58	

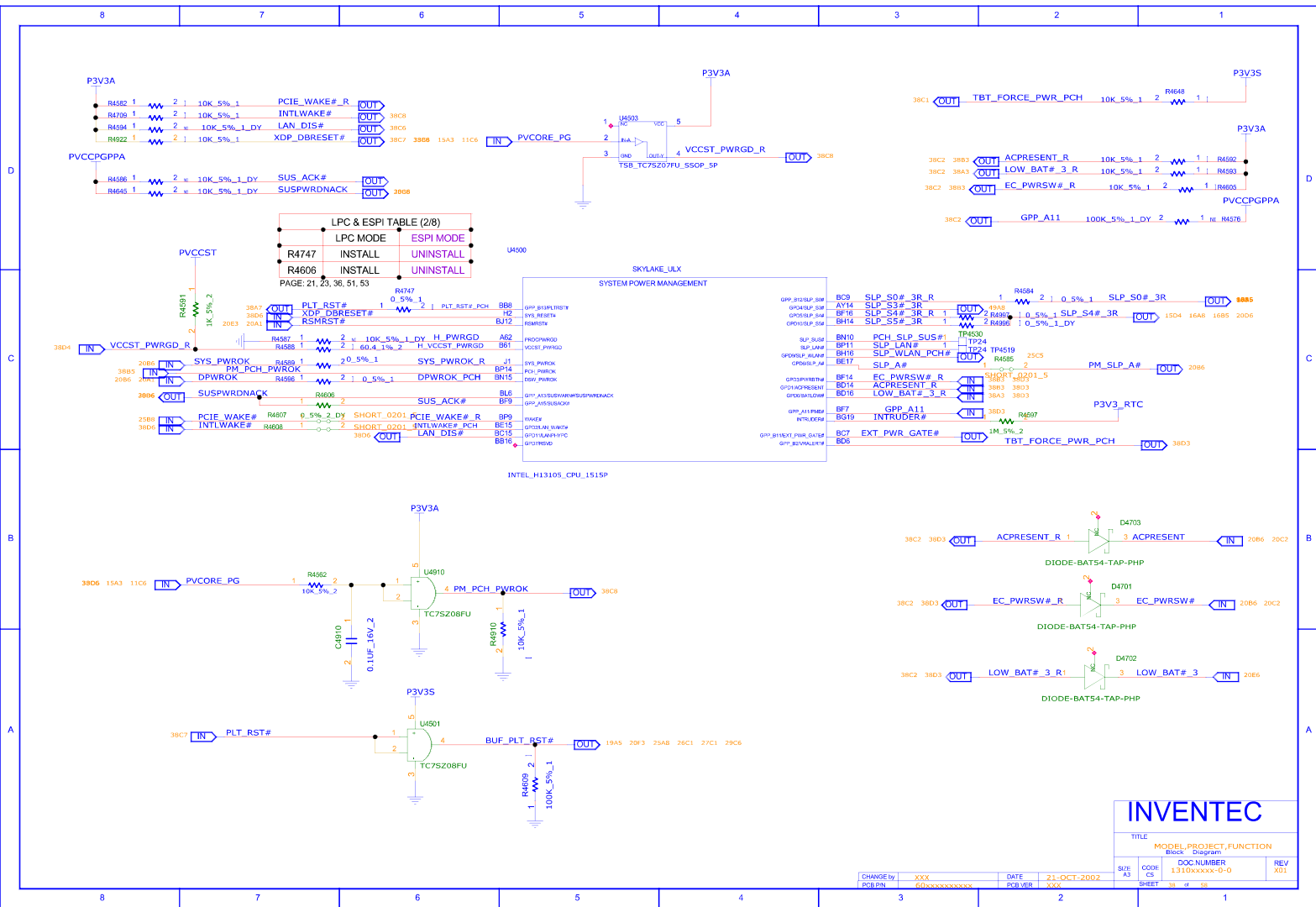
CHANGE BY	XXX	DATE	XXX-OCT-2002
PGS.DN	6R4418XXXXXXXXXX	PGS.VBR	XXX-OCT-2002

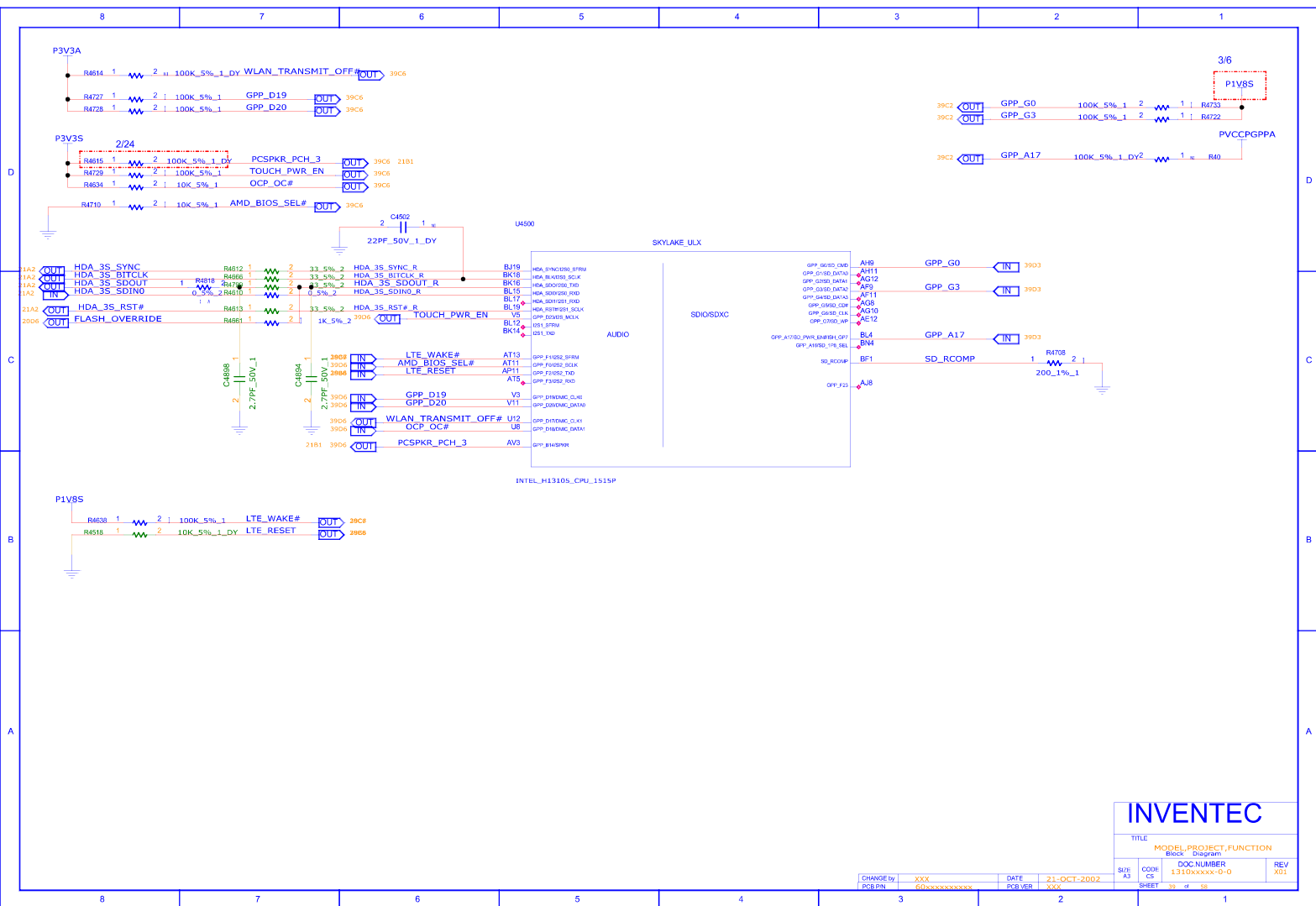




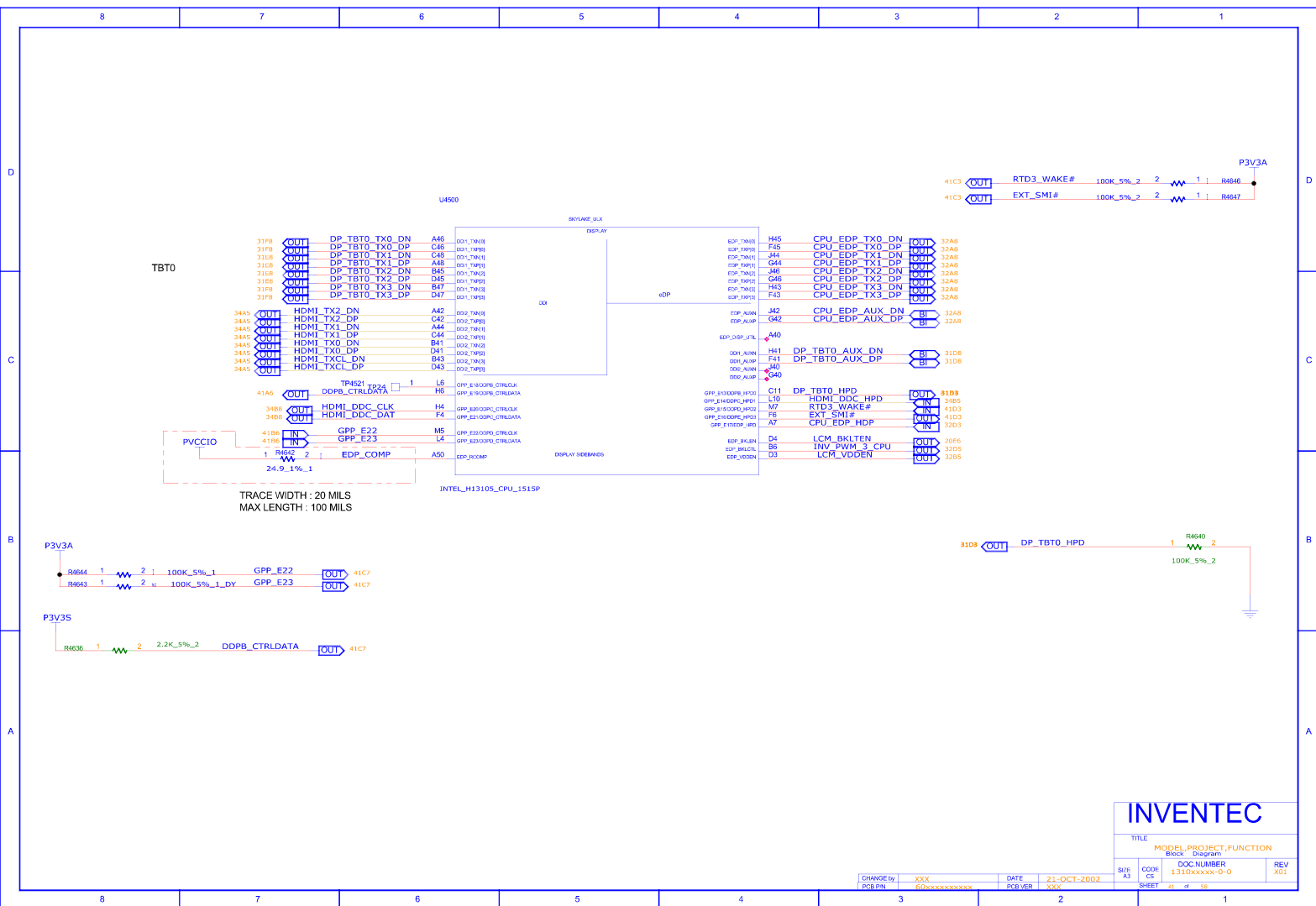


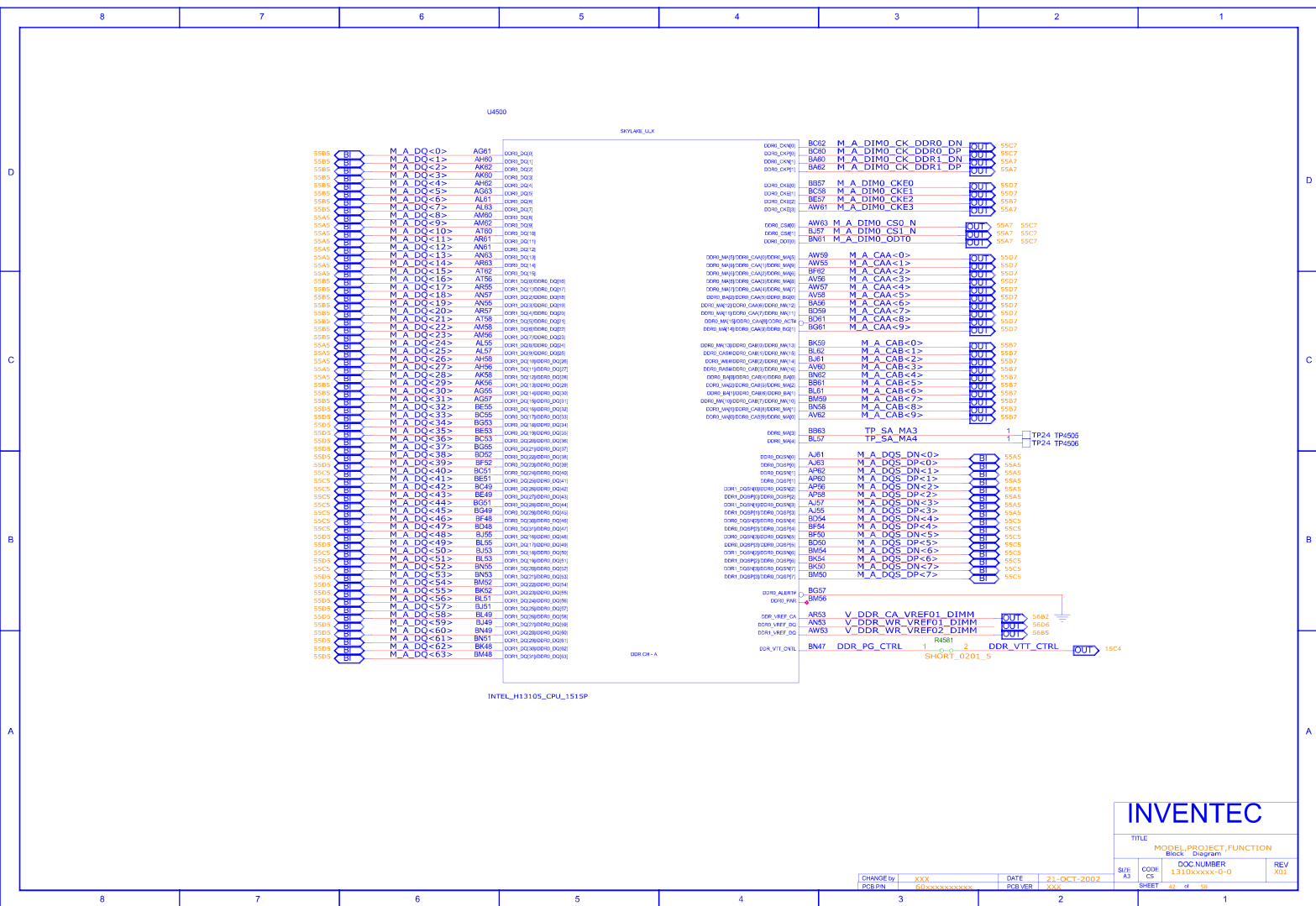


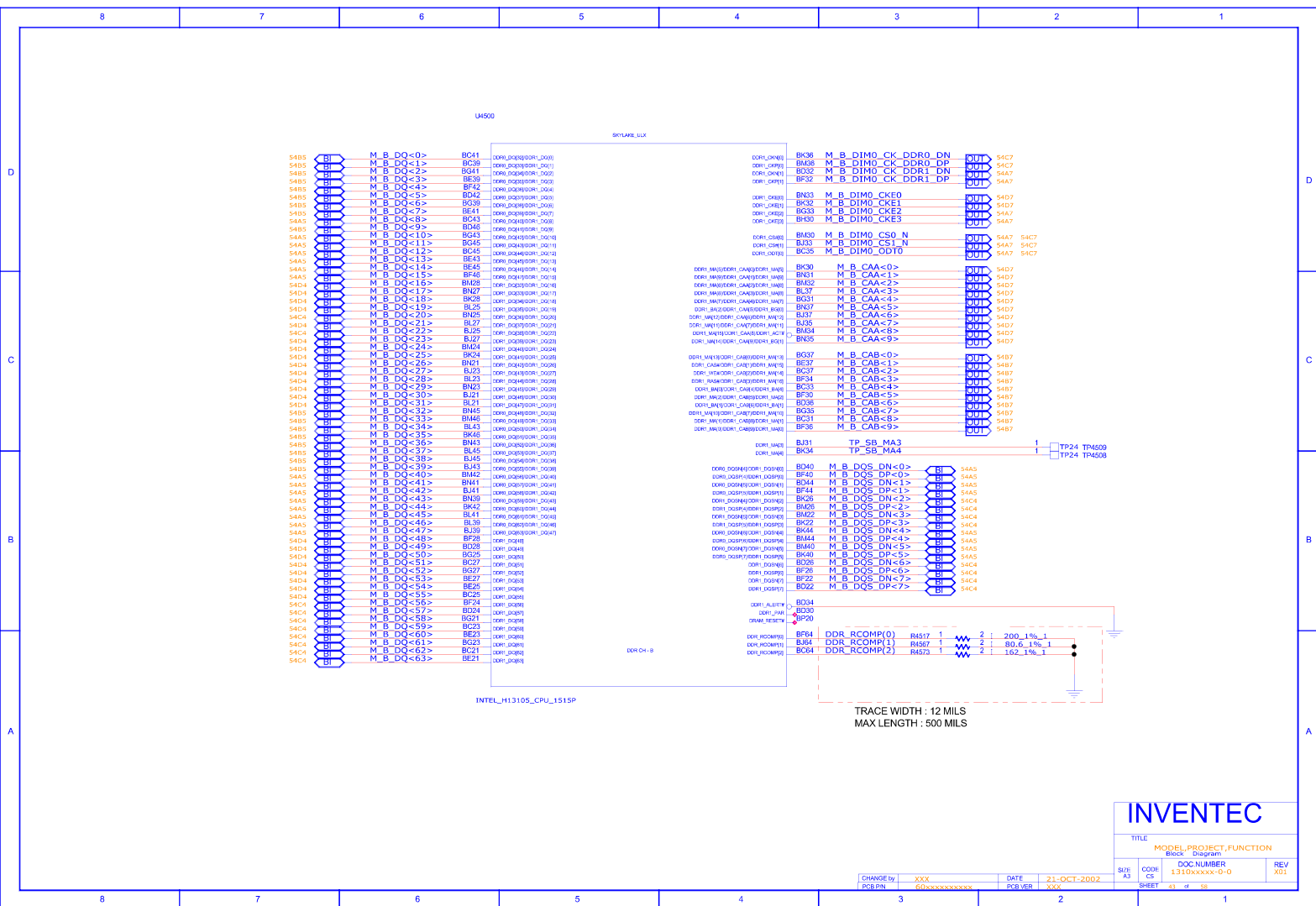


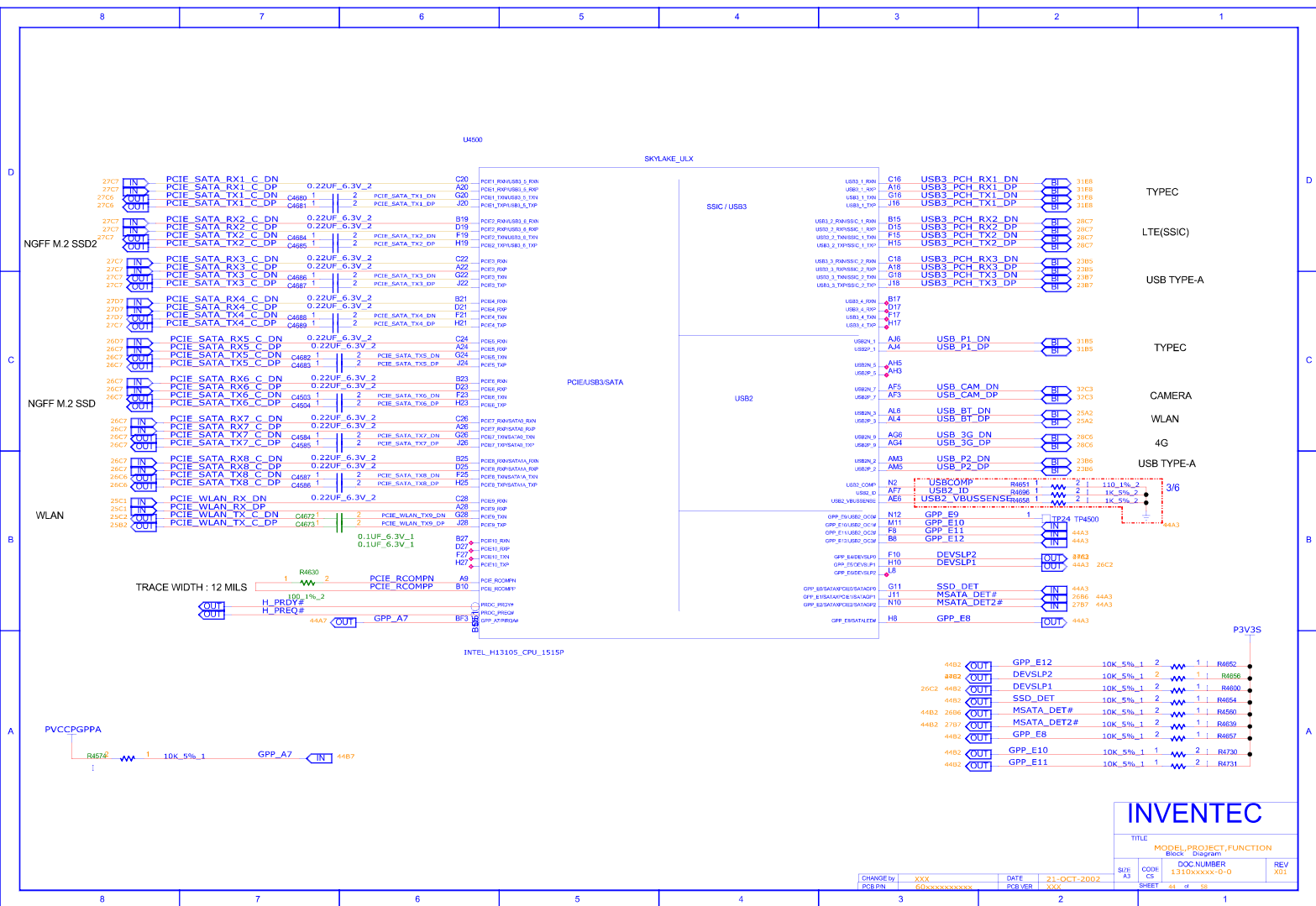


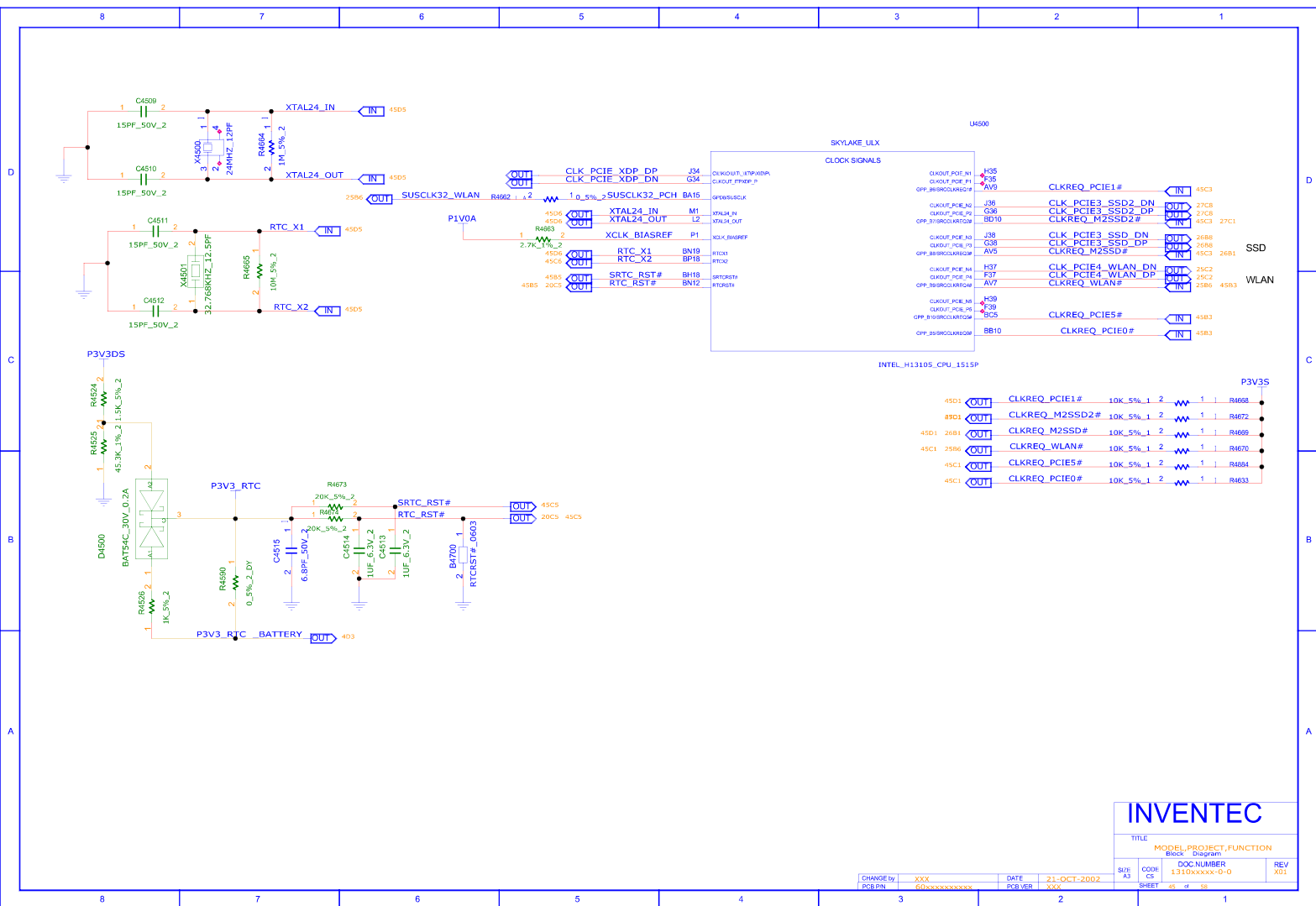


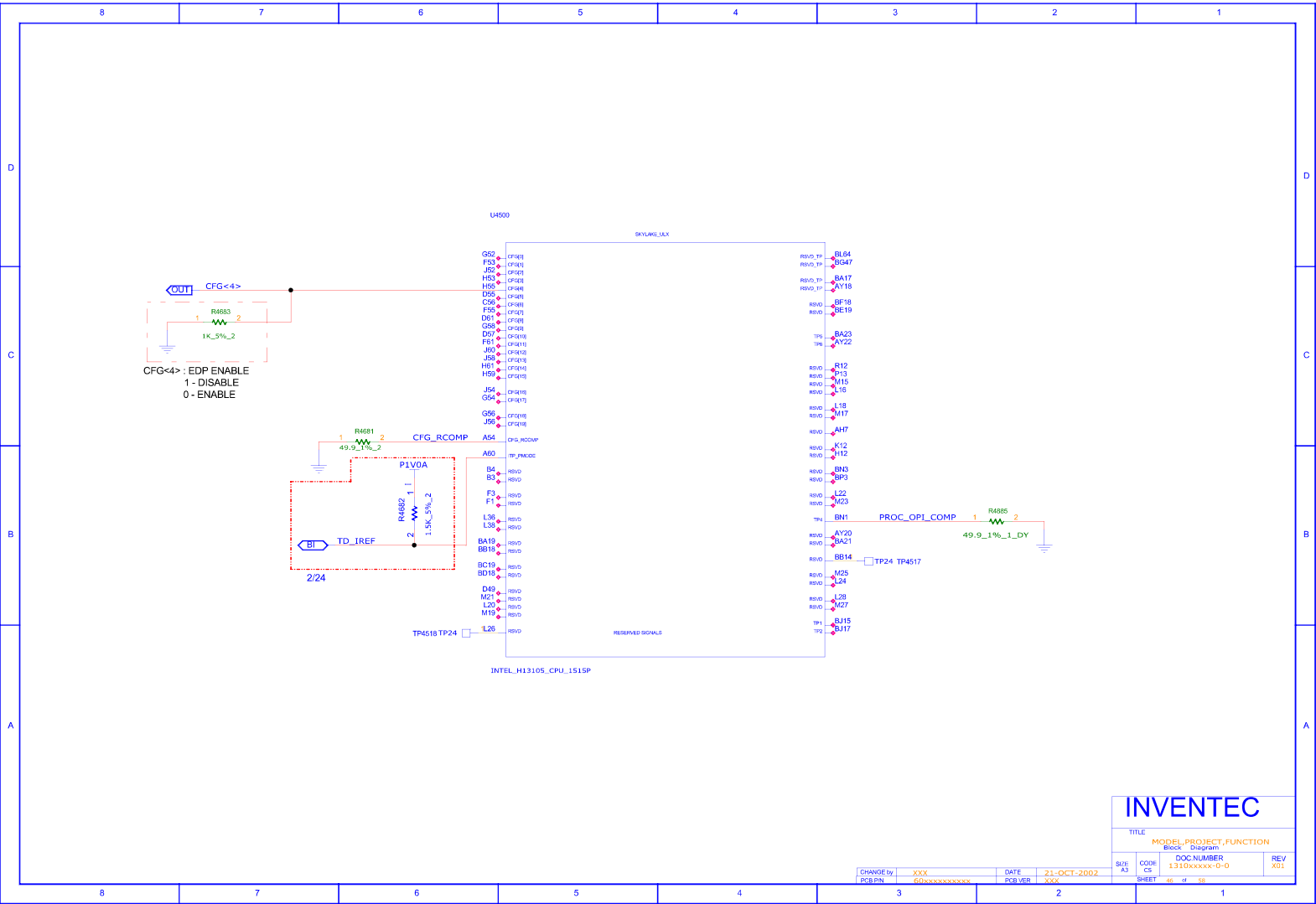


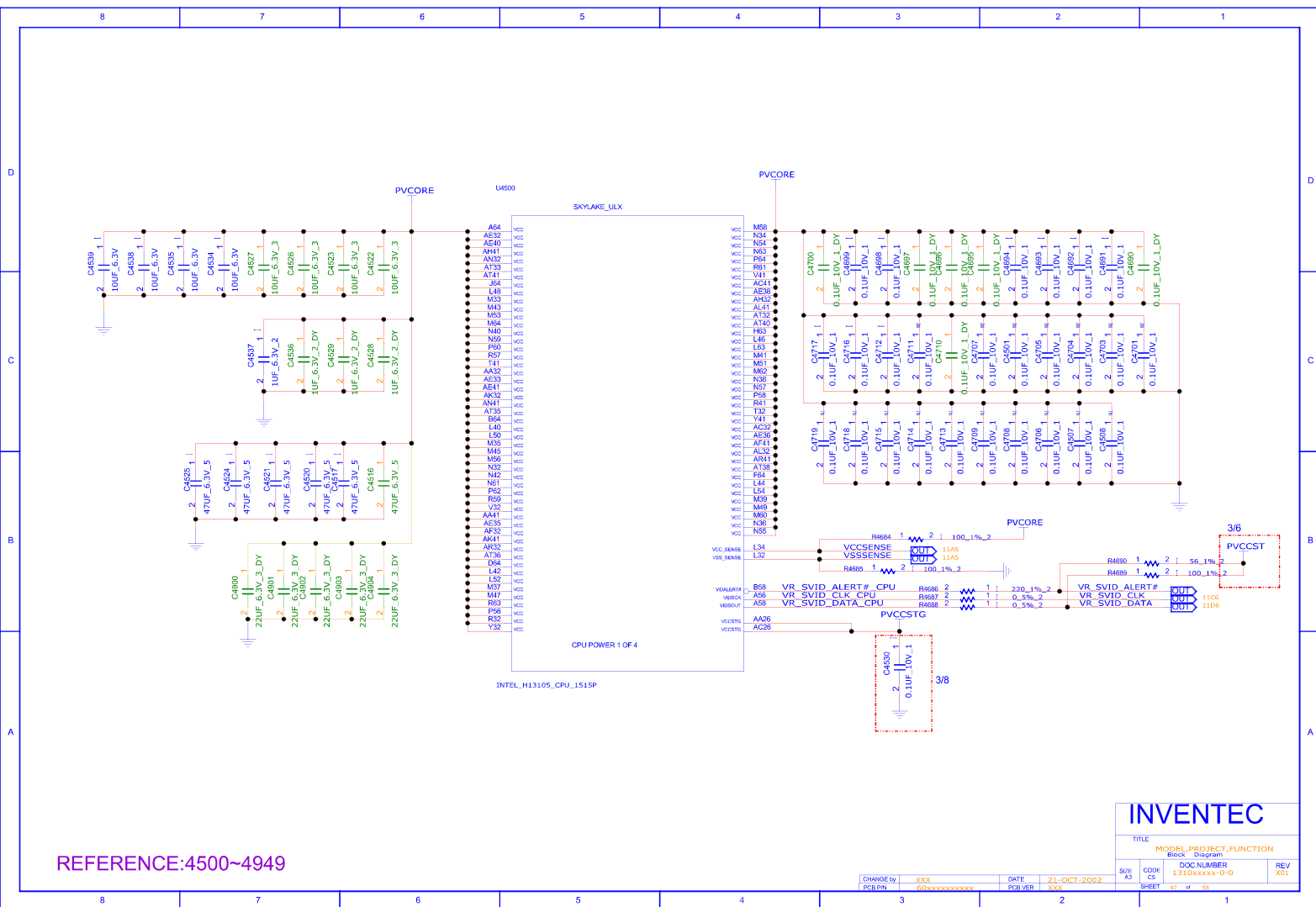


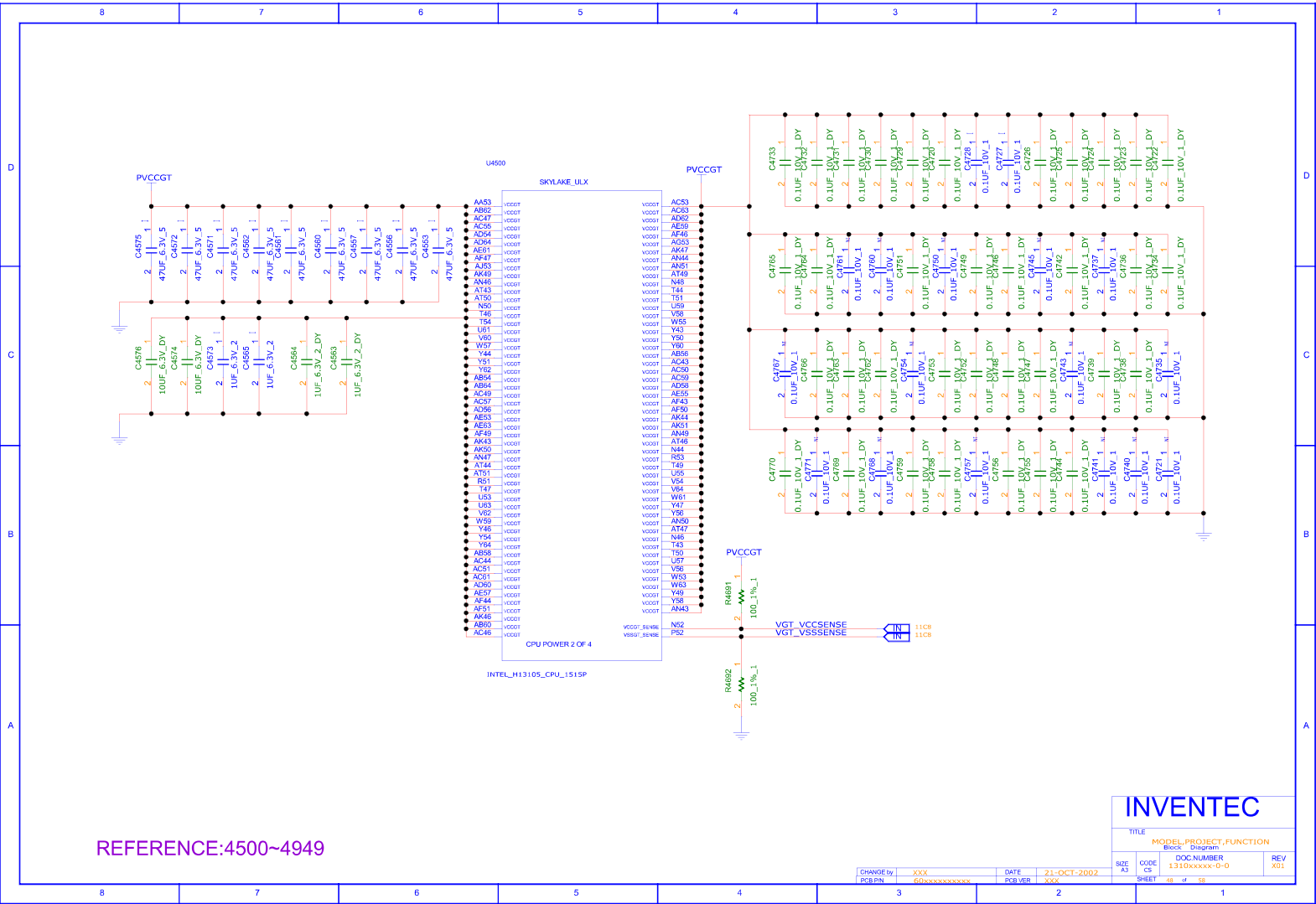


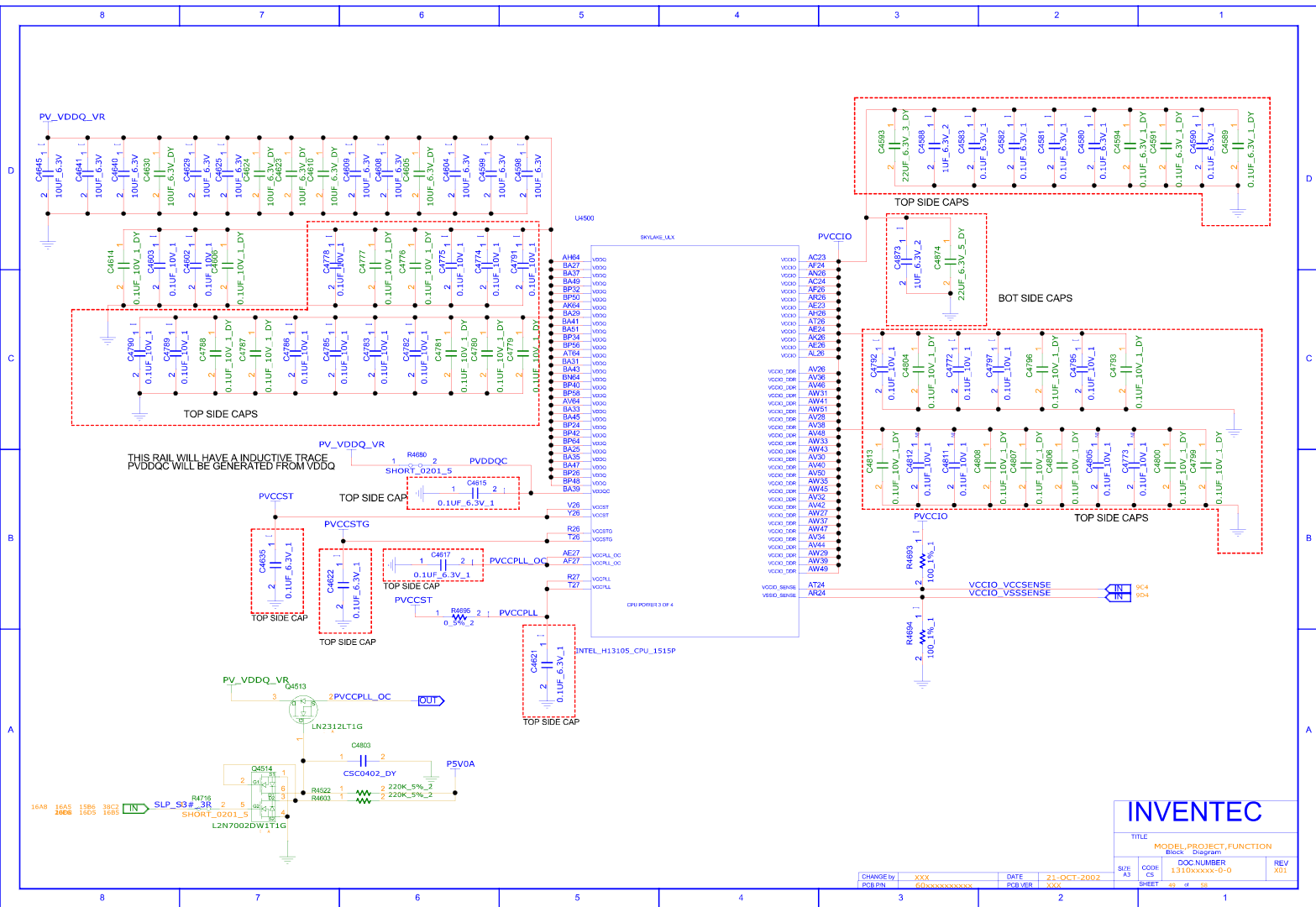






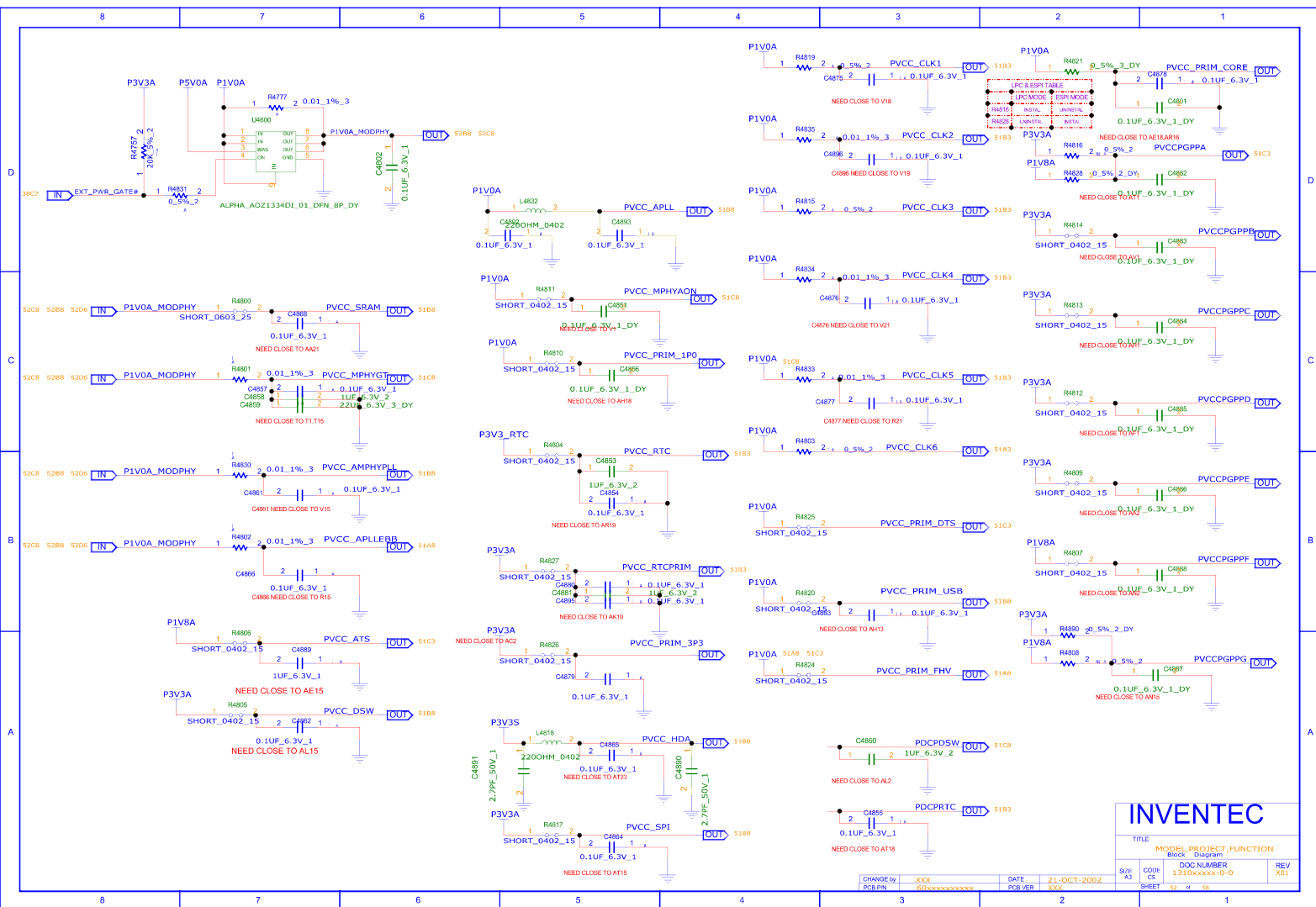


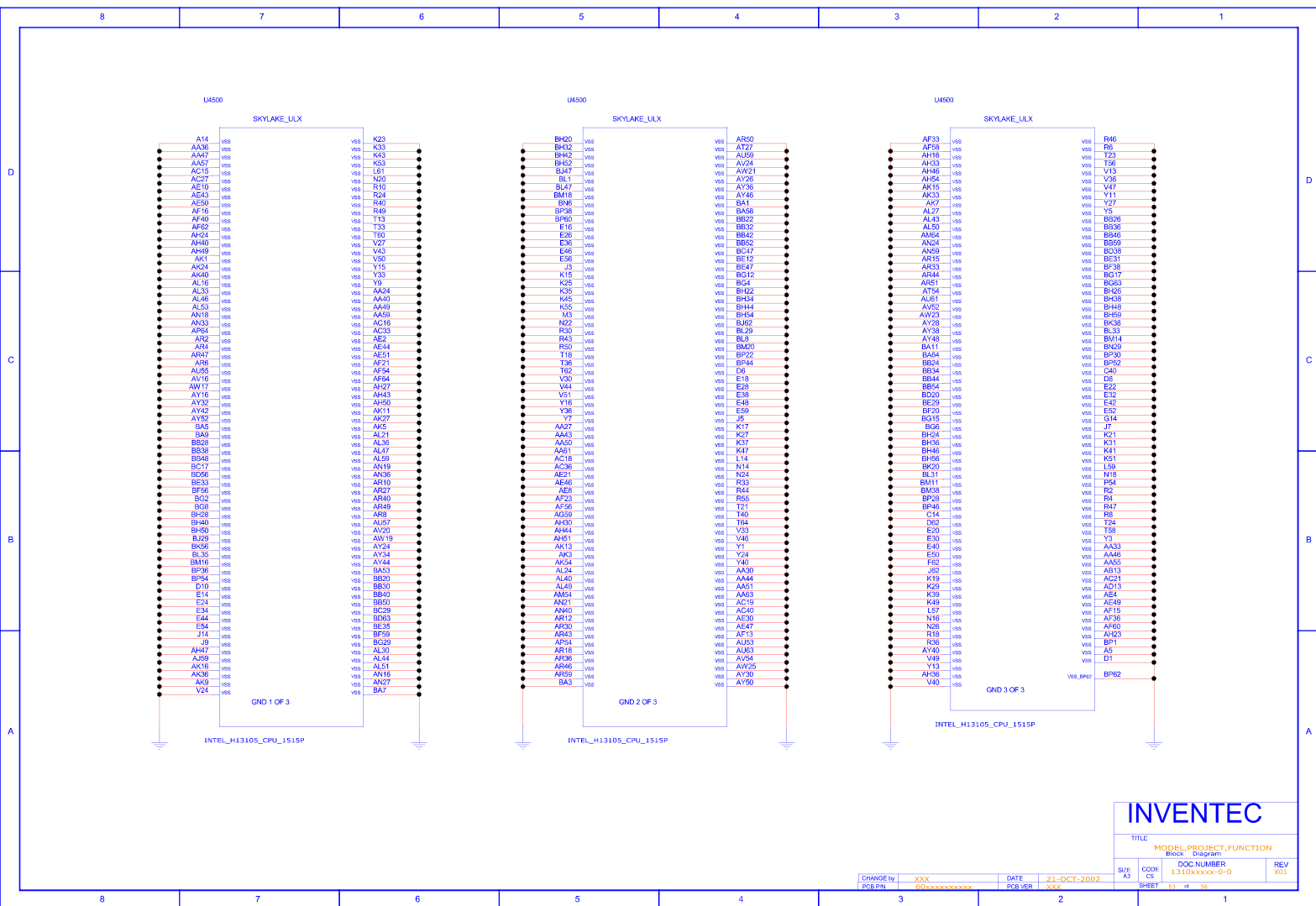


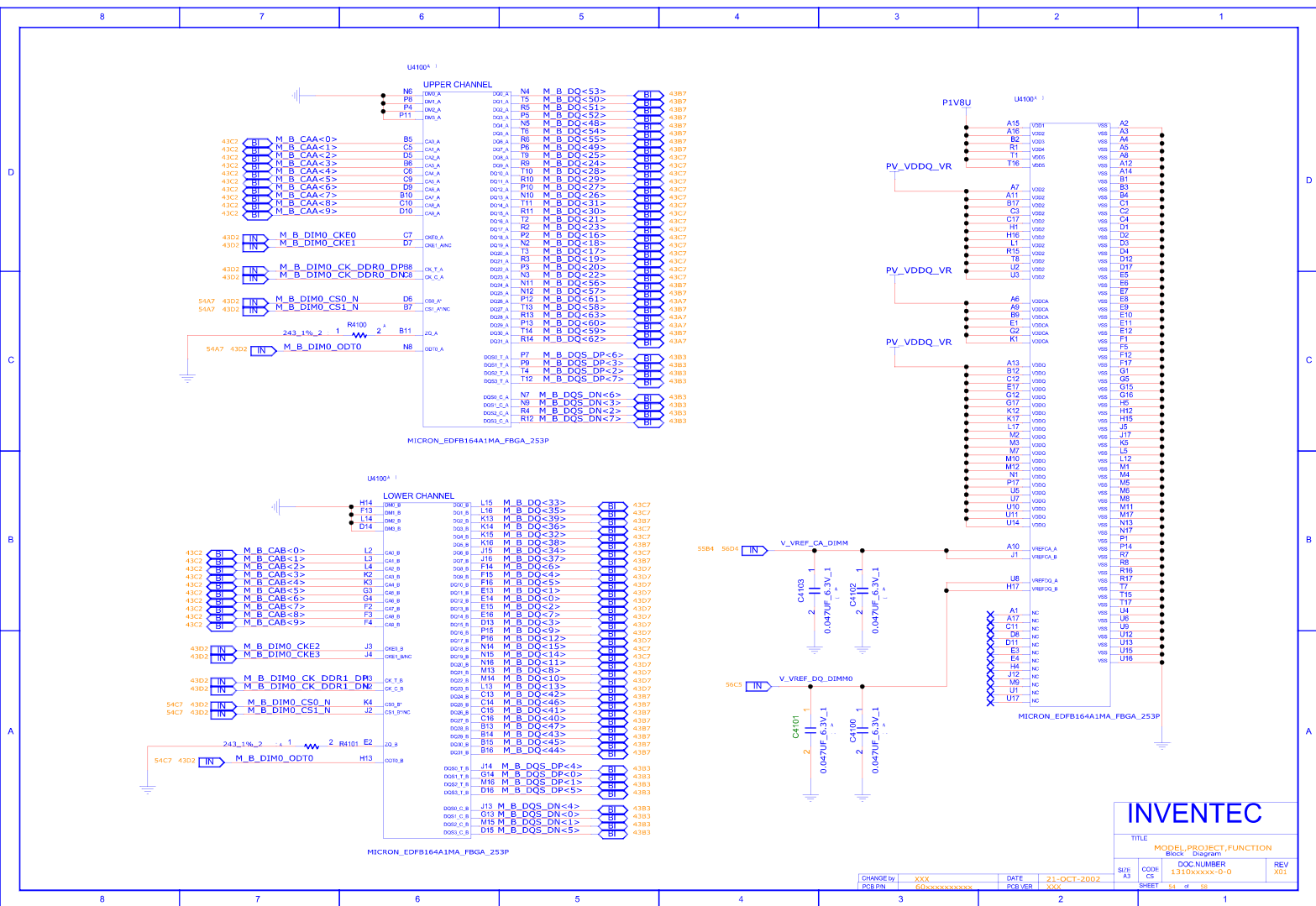


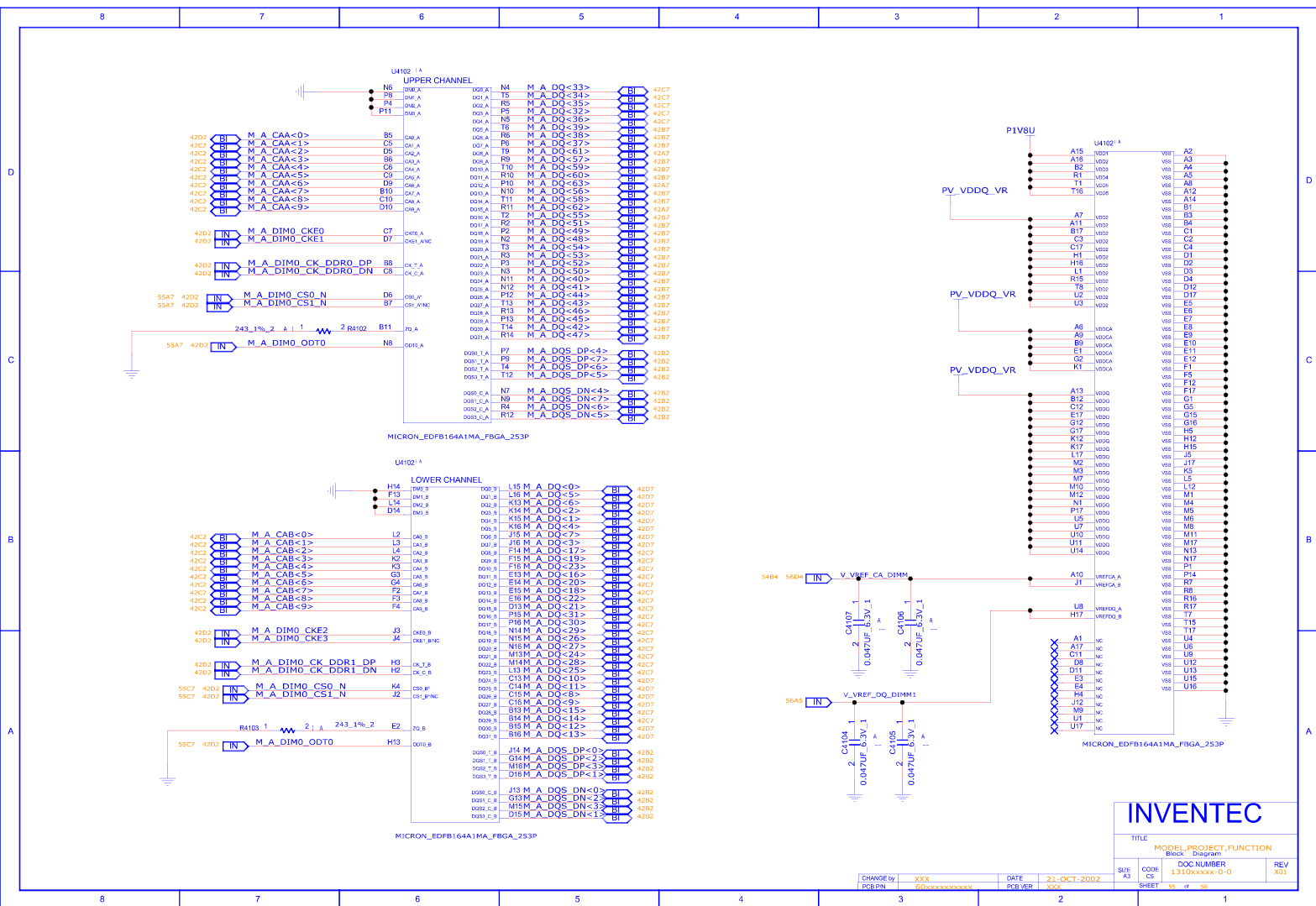




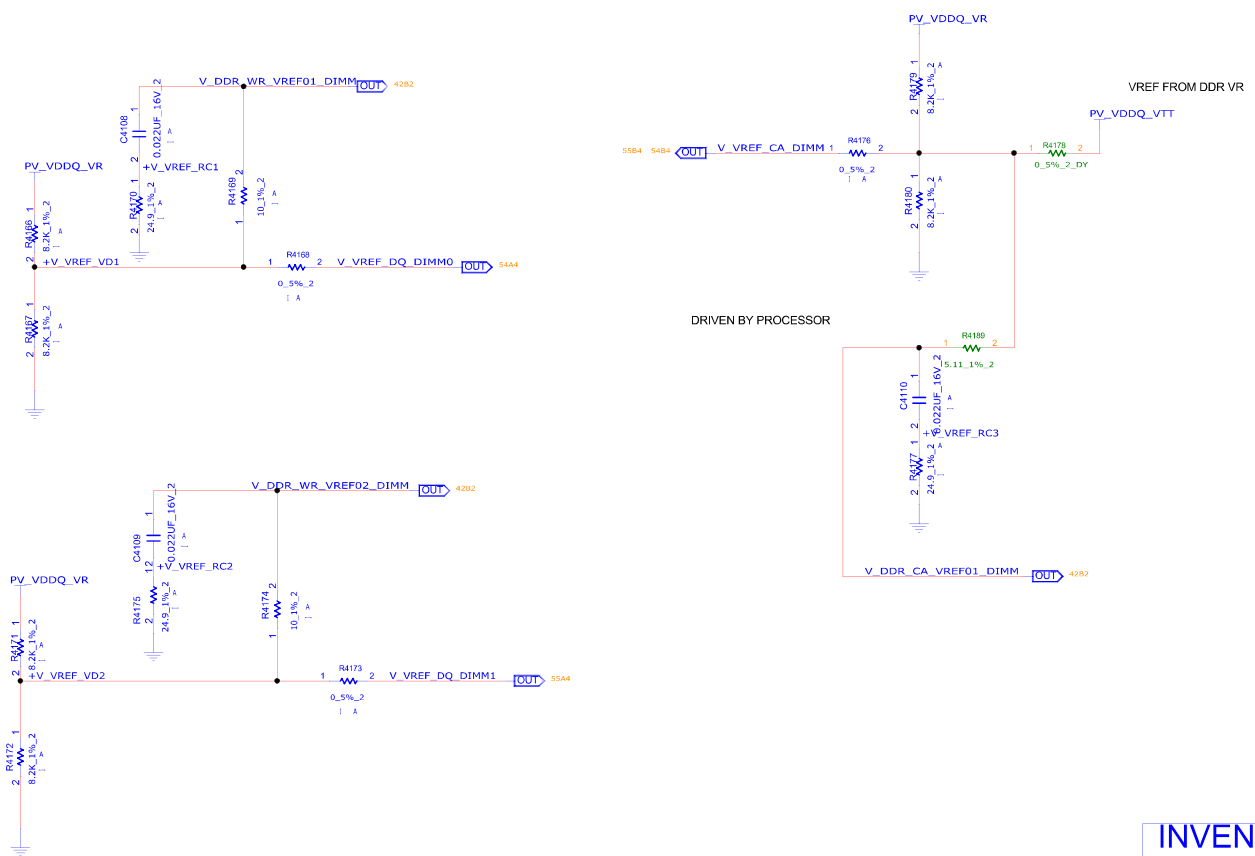








# VREF CIRCUITRY - M1 (VOLTAGE DIVIDER NETWORK) & M3 (DRIVEN BY PROCESSOR) IMPLEMENTATION



**INVENTEC**

TITLE MODEL,PROJECT,FUNCTION

LPDDR3-6 VREF

SIZE CODE DOC NUMBER REV

A3 CS 1310XXXX-0-0 X01

CHANGE BY XXX

DATE 21-OCT-2002

PCB.DN PCB.VBR

SHEET 58 of 58

